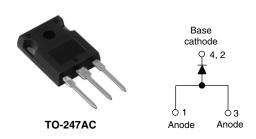




Vishay High Power Products

Input Rectifier Diode, 80 A



PRODUCT SUMMARY				
V _F at 80 A	1.17 V			
I _{FSM}	1450 A			
V_{RRM}	800/1200 V			

DESCRIPTION/FEATURES

The 80EPS...PbF rectifier High Voltage Series has been optimized for very low forward voltage drop, with moderate leakage. The glass passivation technology used has reliable operation up to 150 °C junction temperature.



Typical applications are in input rectification and these products are designed to be used with Vishay HPP switches and output rectifiers which are available in identical package outlines.

This product has been designed and qualified for industrial level.

Compliant to RoHS directive 2002/95/EC.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS VALUES UNITS						
I _{F(AV)}	Sinusoidal waveform	80	A				
V _{RRM}	Range	800/1200	V				
I _{FSM}		1450	A				
V _F	80 A, T _J = 25 °C	1.17	V				
T _J		- 40 to 150	°C				

VOLTAGE RATINGS							
PART NUMBER	V _{RRM} , MAXIMUM PEAK REVERSE VOLTAGE V	V _{RSM} , MAXIMUM NON-REPETITIVE PEAK REVERSE VOLTAGE V	I _{RRM} AT 150 °C mA				
80EPS08PbF	800	900	1				
80EPS12PbF	1200	1300	I				

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum average forward current	I _{F(AV)}	$T_C = 100$ °C, 180 ° conduction half sine wave	80			
Maximum peak one cycle	I _{FSM}	10 ms sine pulse, rated V _{RRM} applied	1450	Α		
non-repetitive surge current		10 ms sine pulse, no voltage reapplied 1500				
Naccina con 124 for fracing	I ² t	10 ms sine pulse, rated V _{RRM} applied	10 500	A ² s		
Maximum I ² t for fusing		10 ms sine pulse, no voltage reapplied	14 000			
Maximum I ² √t for fusing	I ² √t	t = 0.1 ms to 10 ms, no voltage reapplied	105 000	A ² √s		

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^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

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ELECTRICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST COI	NDITIONS	VALUES	UNITS		
Maximum forward voltage drop	V_{FM}	80 A, T _J = 25 °C		1.17	V		
Forward slope resistance	r _t	T _J = 150 °C		3.17	mΩ		
Threshold voltage	V _{F(TO)}			0.73	V		
Maximum reverse leakage current	1	T _J = 25 °C V _B = Rated V _{BBM}		0.1	mA		
Maximum reverse leakage current	IRM	T _J = 150 °C	VR = nateu VRRM	1.0	IIIA		

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL TEST CONDITIONS		VALUES	UNITS		
Maximum junction and storage temperature range		T _J , T _{Stg}		- 40 to 150	°C		
Maximum thermal resistance, junction to case		R_{thJC}	DC operation	0.35			
Maximum thermal resistance, junction to ambient		R_{thJA}		40	°C/W		
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, flat, smooth and greased	0.2			
According to the second state of				6	g		
Approximate weight				0.21	oz.		
Mounting torque	minimum			6 (5)	kgf · cm		
Mounting torque ——	maximum			12 (10)	(lbf · in)		
Marking device			Coop at the TO 247AC (IEDEC)	80EPS08			
			Case style TO-247AC (JEDEC)	80EPS12			

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Input Rectifier Diode, 80 A Vishay High Power Products

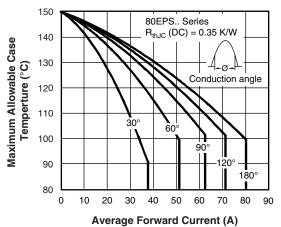


Fig. 1 - Current Rating Characteristics

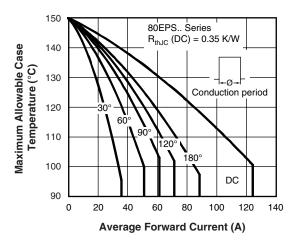


Fig. 2 - Current Rating Characteristics

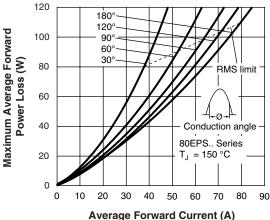


Fig. 3 - Forward Power Loss Characteristics

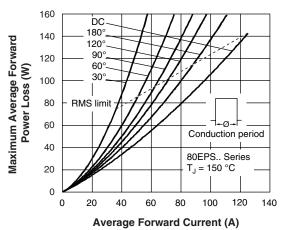
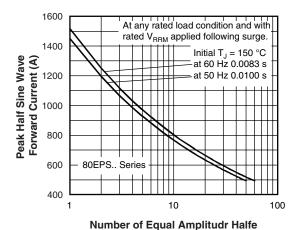


Fig. 4 - Forward Power Loss Characteristics



Cycle Current Pulse (N)
Fig. 5 - Maximum Non-Repetitive Surge Current

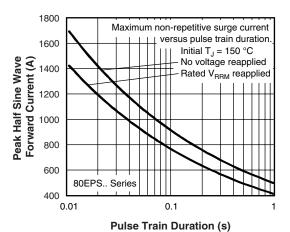


Fig. 6 - Maximum Non-Repetitive Surge Current

80EPS..PbF High Voltage Series

Vishay High Power Products Input Rectifier Diode, 80 A



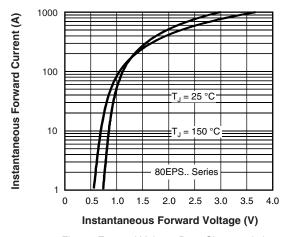


Fig. 7 - Forward Voltage Drop Characteristics

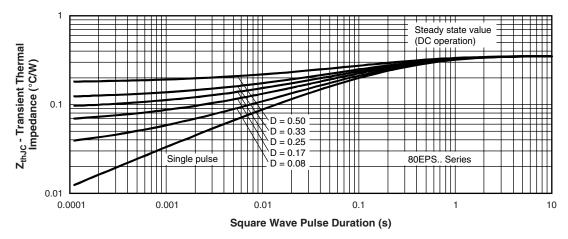


Fig. 8 - Thermal Impedance Z_{thJC} Characteristics

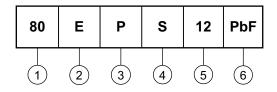


80EPS..PbF High Voltage Series

Input Rectifier Diode, 80 A Vishay High Power Products

ORDERING INFORMATION TABLE

Device code



1 - Current rating (80 = 80 A)

2 - Circuit configuration:

E = Single diode

3 - Package:

P = TO-247AC

4 - Type of silicon:

S = Standard recovery rectifier

08 = 800 V 12 = 1200 V

Voltage ratings

6 - None = Standard production

• PbF = Lead (Pb)-free

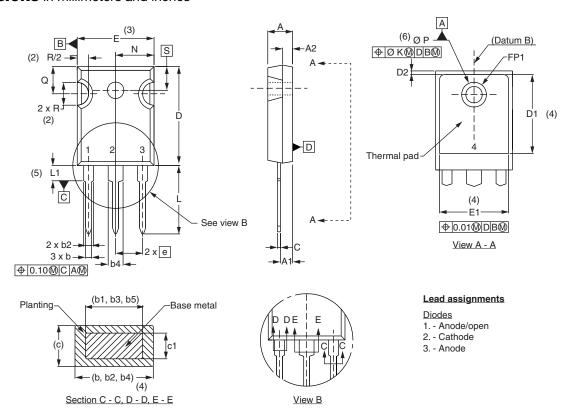
LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?95223</u>					
Part marking information	www.vishay.com/doc?95226				

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Vishay Semiconductors

DIMENSIONS in millimeters and inches



SYMBOL	MILLIM	IETERS	INC	INCHES	
STIVIBUL	MIN.	MAX.	MIN.	MAX.	NOTES
Α	4.65	5.31	0.183	0.209	
A1	2.21	2.59	0.087	0.102	
A2	1.50	2.49	0.059	0.098	
b	0.99	1.40	0.039	0.055	
b1	0.99	1.35	0.039	0.053	
b2	1.65	2.39	0.065	0.094	
b3	1.65	2.37	0.065	0.094	
b4	2.59	3.43	0.102	0.135	
b5	2.59	3.38	0.102	0.133	
С	0.38	0.86	0.015	0.034	
c1	0.38	0.76	0.015	0.030	
D	19.71	20.70	0.776	0.815	3
D1	13.08	-	0.515	-	4

SYMBOL	MILLIN	IETERS	INC	NOTES	
STWIBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	0.51	1.30	0.020	0.051	
E	15.29	15.87	0.602	0.625	3
E1	13.72	-	0.540	-	
е	5.46	BSC	0.215	BSC	
FK	2.54		0.0	0.010	
L	14.20	16.10	0.559	0.634	
L1	3.71	4.29	0.146	0.169	
N	7.62	62 BSC 0.3			
ΦР	3.56	3.66	0.14	0.144	
ФР1	-	6.98	-	0.275	
Q	5.31	5.69	0.209	0.224	
R	4.52	5.49	1.78	0.216	
S	5.51 BSC		0.217	'BSC	

Notes

- (1) Dimensioning and tolerancing per ASME Y14.5M-1994
- (2) Contour of slot optional
- (3) Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body
- (4) Thermal pad contour optional with dimensions D1 and E1
- (5) Lead finish uncontrolled in L1
- (6) Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")
- (7) Outline conforms to JEDEC outline TO-247 with exception of dimension c





Vishay

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