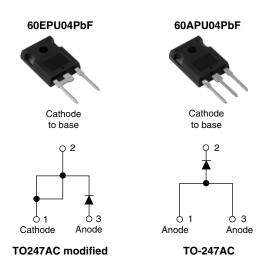




Vishay High Power Products

Ultrafast Soft Recovery Diode, 60 A FRED Pt®



PRODUCT SUMMARY				
t _{rr} 50 ns				
I _{F(AV)}	60 A			
V _R	400 V			

FEATURES

- Ultrafast recovery
- 175 °C operating junction temperature
- Compliant to RoHS directive 2002/95/EC
- Designed and qualified for industrial level



RoHS*

BENEFITS

- Reduced RFI and EMI
- Higher frequency operation
- · Reduced snubbing
- · Reduced parts count

DESCRIPTION/APPLICATIONS

These diodes are optimized to reduce losses and EMI/RFI in high frequency power conditioning systems.

The softness of the recovery eliminates the need for a snubber in most applications. These devices are ideally suited for HF welding, power converters and other applications where switching losses are not significant portion of the total losses.

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Cathode to anode voltage	V _R		400	V
Continuous forward current	I _{F(AV)}	T _C = 127 °C	60	
Single pulse forward current	I _{FSM}	T _C = 25 °C	600	Α
Maximum repetitive forward current	I _{FRM}	Square wave, 20 kHz	120	
Operating junction and storage temperatures	T _J , T _{Stg}		- 55 to 175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V _{BR} , V _R	Ι _R = 100 μΑ	400	-	-	
		I _F = 60 A	-	1.05	1.25	V
Forward voltage	V_{F}	I _F = 60 A, T _J = 175 °C	-	0.87	1.03	
		I _F = 60 A, T _J = 125 °C	-	0.93	1.10	
Reverse leakage current I _R		V _R = V _R rated	=	-	50	μA
		T _J = 150 °C, V _R = V _R rated	-	-	2	mA
Junction capacitance	C _T	V _R = 400 V	-	50	-	pF
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	3.5	-	nH

^{*} Pb containing terminations are not RoHS compliant, exemptions may apply

60EPU04PbF, 60APU04PbF



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DYNAMIC RECOVERY CHARACTERISTICS (T _C = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 \text{ A, } dI_F/dt = 200 \text{ A/}\mu\text{s, } V_R = 30 \text{ V}$		-	50	60	
Reverse recovery time	Reverse recovery time t_{rr}	T _J = 25 °C		-	85	-	ns
		T _J = 125 °C	$I_F = 60 \text{ A}$ $dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_R = 200 \text{ V}$	-	145	-	
Peak recovery current I _{RF}	1	T _J = 25 °C		-	8.8	-	А
	I _{RRM}	T _J = 125 °C		-	15.4	-	A
Reverse recovery charge	Q _{rr}	T _J = 25 °C		=	375	-	nC
		T _J = 125 °C		=	1120	-	

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Thermal resistance, junction to case	R _{thJC}		-	-	0.70	K/W
Thermal resistance, case to heatsink	R _{thCS}	Mounting surface, flat, smooth and greased	-	0.2	-	- r./ vv
Weight			-	5.5	-	g
			-	0.2	-	oz.
Mounting torque			1.2 (10)	-	2.4 (20)	N ⋅ m (lbf ⋅ in)
Marking device		Case style TO-247AC modified		60EPU04		
		Case style TO-247AC		60APU04		



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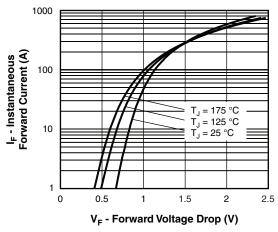


Fig. 1 - Typical Forward Voltage Drop Characteristics

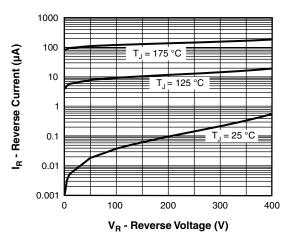


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

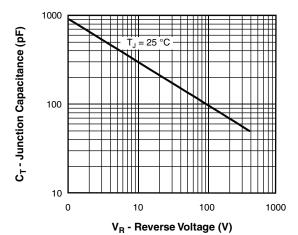


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

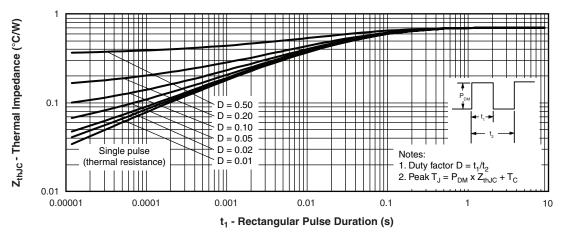


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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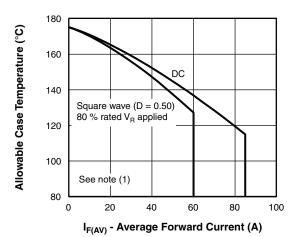


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

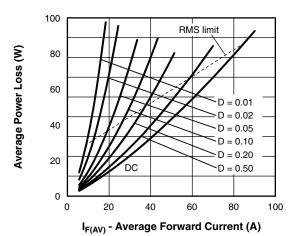


Fig. 6 - Forward Power Loss Characteristics

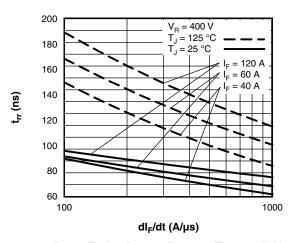


Fig. 7 - Typical Reverse Recovery Time vs. dI_F/dt

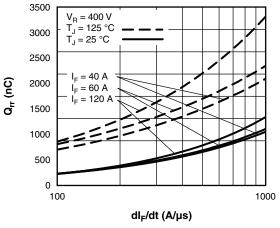


Fig. 8 - Typical Stored Charge vs. dl_F/dt

Note

 $\begin{array}{ll} \mbox{(1)} & \mbox{Formula used: } T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}; \\ Pd = \mbox{Forward power loss} = I_{F(AV)} \times V_{FM} \mbox{ at } (I_{F(AV)}/D) \mbox{ (see fig. 6);} \\ Pd_{REV} = \mbox{Inverse power loss} = V_{R1} \times I_R \mbox{ (1 - D); } I_R \mbox{ at } V_{R1} = 80 \mbox{ \% rated } V_R \mbox{ } \end{array}$



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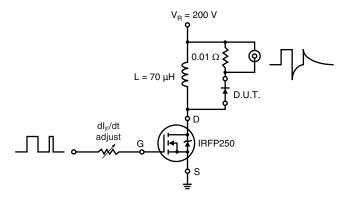
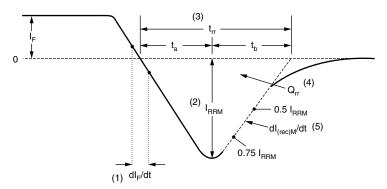


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dI_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RBM} and 0.50 I_{RBM} extrapolated to zero current.
- (4) \mathbf{Q}_{rr} area under curve defined by \mathbf{t}_{rr} and \mathbf{I}_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 10 - Reverse Recovery Waveform and Definitions

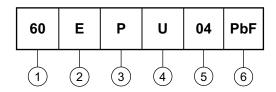
60EPU04PbF, 60APU04PbF

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ORDERING INFORMATION TABLE

Device code



1 - Current rating (60 = 60 A)

2 - Circuit configuration:

E = Single diode

A = Single diode, 3 pins

Package:

P = TO-247AC (modified)

4 - Type of silicon:

U = Ultrafast recovery

5 - Voltage rating (04 = 400 V)

6 - • None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions	TO-247AC modified	www.vishay.com/doc?95253		
Differsions	TO-247AC	www.vishay.com/doc?95223		
Part marking information	TO-247AC modified	www.vishay.com/doc?95255		
Fait marking information	TO-247AC	www.vishay.com/doc?95226		

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