PROTECTION PRODUCTS - MicroClamp®

Description
The μClamp®2512T TVS diode is specifically designed to meet the performance requirements of Gigabit Ethernet interfaces. They are designed to protect sensitive PHY chips from damage or upset due to electrostatic discharge (ESD), lightning, electrical fast transients (EFT), and cable discharge events (CDE). The μClamp2512T is constructed using Semtech’s low voltage EPD process technology. The EPD process provides low operating voltages with significant reductions in leakage current and capacitance over silicon-avalanche diode processes. The device features low variation in capacitance over bias for stable operation on GbE lines. This means the μClamp2512T will introduce zero traffic frame errors on GbE interfaces up to a PHY temperature of 120°C (100M Cat 5/5e Cable). The μClamp2512T also features high surge capability and is designed to be placed between the magnetics and the PHY chip. In this configuration, the device can withstand intra-building lightning surges per Telcordia GR-1089.

The μClamp2512T is in a 8-pin SLP2010N8T package. It measures 2.0 x 1.0 x 0.4mm. The leads are spaced at a pitch of 0.5mm and are finished with lead-free NiPdAu. Each device will protect two line pairs operating at 2.5 volts. It gives the designer the flexibility to protect multiple lines in applications where space is at a premium. The small size and easy layout of the μClamp2512T make it ideal for use in RJ-45 connectors with integrated magnetics.

Features
- High ESD withstand Voltage: +/-30kV (Contact/Air) per IEC 61000-4-2
- Able to withstand over 1000 ESD strikes per IEC 61000-4-2 Level 4
- Flow-through design simplifies layout
- Protects two line pairs
- Low reverse current: 10nA typical (VR=2.5V)
- Low variation in capacitance vs. bias voltage: 1.3pF Typical (VR = 0 to 2.5V)
- Working voltage: 2.5V
- Solid-state silicon-avalanche technology

Mechanical Characteristics
- SLP2010N8T package
- Pb-Free, Halogen Free, RoHS/WEEE Compliant
- Nominal Dimensions: 2.0 x 1.0 x 0.4 mm
- Lead Finish: NiPdAu
- Molding compound flammability rating: UL 94V-0
- Marking: Marking code
- Packaging: Tape and Reel

Applications
- 10/100/1000 Ethernet
- Integrated magnetics/RJ-45 connectors
- LAN/WAN Equipment
- Security Cameras
- Industrial Controls
- Notebooks & Desktop Computers

Typical Application

Schematic & PIN Configuration

GBE Integrated Magnetics Module

SLP2010N8T
## Absolute Maximum Rating

<table>
<thead>
<tr>
<th>Rating</th>
<th>Symbol</th>
<th>Value</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Peak Pulse Power (tp = 8/20μs)</td>
<td>( P_{pk} )</td>
<td>100</td>
<td>Watts</td>
</tr>
<tr>
<td>Maximum Peak Pulse Current (tp = 8/20μs)</td>
<td>( I_{pp} )</td>
<td>10</td>
<td>Amps</td>
</tr>
<tr>
<td>ESD per IEC 61000-4-2 (Air)</td>
<td>( V_{ESD} )</td>
<td>+/- 30</td>
<td>kV</td>
</tr>
<tr>
<td>ESD per IEC 61000-4-2 (Contact)</td>
<td></td>
<td>+/- 30</td>
<td>kV</td>
</tr>
<tr>
<td>Operating Temperature</td>
<td>( T_j )</td>
<td>-40 to +85</td>
<td>°C</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>( T_{STG} )</td>
<td>-55 to +150</td>
<td>°C</td>
</tr>
</tbody>
</table>

## Electrical Characteristics (T=25°C)

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Symbol</th>
<th>Conditions</th>
<th>Minimum</th>
<th>Typical</th>
<th>Maximum</th>
<th>Units</th>
</tr>
</thead>
<tbody>
<tr>
<td>Reverse Stand-Off Voltage</td>
<td>( V_{RWM} )</td>
<td>( I_{pp} = 2μA )</td>
<td>2.7</td>
<td>3.1</td>
<td>3.6</td>
<td>V</td>
</tr>
<tr>
<td>Punch-Through Voltage</td>
<td>( V_{PT} )</td>
<td>( I_{PT} = 2μA )</td>
<td>2.7</td>
<td>3.1</td>
<td>3.6</td>
<td>V</td>
</tr>
<tr>
<td>Snap-Back Voltage</td>
<td>( V_{SB} )</td>
<td>( I_{SB} = 50mA )</td>
<td>2.8</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Reverse Leakage Current</td>
<td>( I_{R} )</td>
<td>( V_{RWM} = 2.5V )</td>
<td>0.01</td>
<td>0.05</td>
<td></td>
<td>μA</td>
</tr>
<tr>
<td>Clamping Voltage</td>
<td>( V_{C} )</td>
<td>( I_{pp} = 1A, ) tp = 8/20μs</td>
<td>4.8</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Clamping Voltage</td>
<td>( V_{C} )</td>
<td>( I_{pp} = 10A, ) tp = 8/20μs</td>
<td>10.2</td>
<td></td>
<td></td>
<td>V</td>
</tr>
<tr>
<td>Variation in capacitance with reverse bias ( ^1 )</td>
<td></td>
<td>Pins 1, 8 to 2, 7 and pins 3, 6 to 4, 5</td>
<td>VR = 0 to 2.5V</td>
<td>( f = 1MHz )</td>
<td>1.3</td>
<td>pF</td>
</tr>
<tr>
<td>Junction Capacitance</td>
<td>( C_{j} )</td>
<td>Pins 1, 8 to 2, 7 and pins 3, 6 to 4, 5</td>
<td>VR = 2.5V, ( f = 1MHz )</td>
<td>4.5</td>
<td>6</td>
<td>pF</td>
</tr>
</tbody>
</table>

Notes:
1) This parameter guaranteed by design and characterization and is not production tested
Non-Repetitive Peak Pulse Power vs. Pulse Time

Peak Pulse Power - P_{pp} (kW)

Pulse Duration - t_p (s)

Clamping Voltage vs. Peak Pulse Current

Clamping Voltage - V_C (V)

Peak Pulse Current - I_{pp} (A)

Normalized Junction Capacitance vs. Reverse Voltage

Normalized Junction Capacitance - C_j (pF)

Reverse Voltage - V_R (V)

Typical Insertion Loss (S21)

Insertion Loss - S21 (dB)

Frequency - f (GHz)

ESD Clamping

ESD Clamping

Waveform Parameters:
- t_r = 8 μs
- t_d = 20 μs

Note: Data is taken with a 10x attenuator
Ethernet systems with connections external to the building are subject to high-level transient threats. This type of equipment may even be required to meet the surge immunity requirements of Telcordia GR-1089. Reliable protection of the Ethernet transceiver requires a device that can absorb the expected transient energy, clamp the incoming surge to a safe level, and yet remain transparent to the system under normal operation. The uClamp2512T has been designed to meet these demanding requirements. Typical IEEE 802.3 template test results for a GbE circuit with uClamp2512T are shown in Figure 2.

**Transient Protection**

When designing Ethernet protection, the entire system must be considered. An Ethernet port includes interface magnetics in the form of transformers and common mode chokes. Transformers and chokes can be discrete components, but integrated solutions that include the RJ-45 connector, resistors, capacitors, and protection are also available. In either case, the transformer will provide a high level of common mode isolation to external voltages, but no protection for metallic (line-to-line) surges. During a metallic transient event, current will flow into one line, through the transformer and back to the source. As the current flows, it charges the windings of the transformer on the line side. Once the surge is removed, the windings on the line side will stop charging and will transfer its stored energy to the IC side where the PHY IC is located. The magnitude and duration of the surge is attenuated by the inductance of the magnetics. The amount of attenuation will vary by vendor and configuration of the magnetics. It is this transferred energy that must be clamped by the protection circuitry.

A typical protection scheme which utilizes the uClamp2512T is shown in Figure 1. One device is placed across two line pairs and is located on the PHY side of the transformer as close to the magnetics as possible. This is done to minimize parasitic inductance and improve clamping performance. In this design, the isolation voltage of the transformer is relied upon to suppress common mode lightning surges. High voltage capacitors and resistors are commonly utilized from the center tap to ground to aid in transient protection. Metallic surges will be transferred in some form to the PHY side and clamped by the uClamp2512T. The uClamp2512T will turn on when the voltage across it exceeds the punch-through voltage of the device. Low voltage turn on is important since many PHY chips have integrated ESD protection structures. These structures are for protection of the device during manufacture and are not designed to handle large amounts of energy. Should they turn on before the external protection, they can be damaged resulting in failure of the PHY chip.
Figure 2 - Typical IEEE 802.3 Template Test Results (With uClamp2512T)
PROTECTION PRODUCTS
Applications Information - Spice Model

Spice Model

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Unit</th>
<th>D1 (TVS)</th>
<th>D2 (TVS)</th>
</tr>
</thead>
<tbody>
<tr>
<td>IS</td>
<td>Amp</td>
<td>1E-20</td>
<td>1E-20</td>
</tr>
<tr>
<td>BV</td>
<td>Volt</td>
<td>2.2</td>
<td>2.2</td>
</tr>
<tr>
<td>VJ</td>
<td>Volt</td>
<td>0.7</td>
<td>0.7</td>
</tr>
<tr>
<td>RS</td>
<td>Ohm</td>
<td>0.3</td>
<td>0.3</td>
</tr>
<tr>
<td>IBV</td>
<td>Amp</td>
<td>1E-3</td>
<td>1E-3</td>
</tr>
<tr>
<td>CJO</td>
<td>Farad</td>
<td>12E-12</td>
<td>12E-12</td>
</tr>
<tr>
<td>TT</td>
<td>sec</td>
<td>2.541E-9</td>
<td>2.541E-9</td>
</tr>
<tr>
<td>M</td>
<td>–</td>
<td>0.05</td>
<td>0.05</td>
</tr>
<tr>
<td>N</td>
<td>–</td>
<td>1.1</td>
<td>1.1</td>
</tr>
<tr>
<td>EG</td>
<td>eV</td>
<td>1.11</td>
<td>1.11</td>
</tr>
</tbody>
</table>
**PROTECTION PRODUCTS**

Outline Drawing - SLP2010N8T

Outline Drawing of uClamp2512T

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**NOTES:**

1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).

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**LAND PATTERN - SLP2010N8T**

Land Pattern of uClamp2512T

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**NOTES:**

1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).

2. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY’S MANUFACTURING GUIDELINES ARE MET.
**PROTECTION PRODUCTS**

**Marking Code**

PIN 1 INDICATOR

2512T YYWW

YYWW = Date Code

**Ordering Information**

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Working Voltage</th>
<th>Qty per Reel</th>
<th>Reel Size</th>
</tr>
</thead>
<tbody>
<tr>
<td>uClamp2512T.TCT</td>
<td>2.5V</td>
<td>3,000</td>
<td>7 Inch</td>
</tr>
</tbody>
</table>

Notes:
1) MicroClamp, uClamp and μClamp are trademarks of Semtech Corporation

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**Tape and Reel Specification**

Tape Width

<table>
<thead>
<tr>
<th>B, (Max)</th>
<th>D</th>
<th>D1</th>
<th>E</th>
<th>F</th>
<th>P</th>
<th>PO</th>
<th>P2</th>
<th>T</th>
<th>W</th>
</tr>
</thead>
<tbody>
<tr>
<td>8 mm</td>
<td>4.2 mm (.165)</td>
<td>1.5 + 0.1 mm (.059 + .005)</td>
<td>0.4 mm (.025)</td>
<td>1.750±.10 mm (.069±.004)</td>
<td>3.5±0.05 mm (.138±.002)</td>
<td>4.0±.010 mm (.157±.004)</td>
<td>4.0±.010 mm (.157±.004)</td>
<td>2.0±.010 mm (.079±.002)</td>
<td>0.254±.024 mm (.010±.001)</td>
</tr>
</tbody>
</table>

**Device Orientation in Tape**

**Contact Information**

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