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UPD78F0743MC-CAA-AX

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Specification

| Core | |
|---------------------------------|---------------------------------|
| CPU | 78K0 (8-bit) |
| Main oscillator | 10 MHz external, 4 MHz internal |
| Sub-oscillator | 30 kHz internal |
| Clock supervising | — |
| Internal clock frequency (max.) | 20 MHz |
| Instruction cycle time (min.) | 100 ns |
| Interrupt channels | 7 external, 12 internal |

| Memory | |
|--|------------|
| Program memory | 8 KB flash |
| RAM size | 512 byte |
| Non-volatile memory size (EEPROM or emulation) | 0 byte |
| ROM correction points | — |

| Interfaces | |
|--------------------------------------|-------|
| Total number of I/Os | 16 |
| Open drain outputs | — |
| CAN | — |
| UART (asynchronous serial interface) | 1 ch. |
| LIN | — |
| CSI (clocked serial interface) | — |
| I ² C | 1 ch. |
| IEbus | — |
| On-chip debug interface | ✓ |

| Peripherals | |
|------------------------|---------------------------------------|
| LCD controller/driver | — |
| A/D converter (10-bit) | 6 ch. |
| Hardware multiplier | 8x8->16, 16x16->32 |
| Hardware divider | n/a |
| DMA | — |
| Low Voltage Indicator | ✓ |
| Power-On Clear | ✓ |
| Other Peripherals | Comparator 3 channels, DALI interface |

| Timers | |
|-----------------|-------|
| Timers (8-bit) | 2 ch. |
| Timers (16-bit) | 3 ch. |
| Interval timers | — |
| Special timers | — |
| Watchdog timers | 1 ch. |