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[Applications](#)
[Development Tools](#)
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Products > MPU & MCU > Product Lineup > 78K0S, 78K0 General Purpose - All products with internal flash memory type (All Flash®) > 78K0/KA2-L >

## UPD78F0567MC-CAA-AX

[Specification](#)
[Documentation](#)
[Development Tools](#)

### Specification

Core	
CPU	78K0 (8-bit)
Main oscillator	10 MHz external, 8 MHz internal
Sub-oscillator	30 kHz internal
Clock supervising	—
Internal clock frequency (max.)	10 MHz
Instruction cycle time (min.)	100 ns
Interrupt channels	4 external, 10 internal

Memory	
Program memory	16 KB flash
RAM size	768 byte
Non-volatile memory size (EEPROM or emulation)	0 byte
ROM correction points	—

Interfaces	
Total number of I/Os	16
Open drain outputs	—
CAN	—
UART (asynchronous serial interface)	1 ch.
LIN	—
CSI (clocked serial interface)	—
I <sup>2</sup> C	1 ch.
IEbus	—
On-chip debug interface	✓

Peripherals	
LCD controller/driver	—
A/D converter (10-bit)	6 ch.
Hardware multiplier	n/a
Hardware divider	n/a
Width of real-time output port	1 ch.
DMA	—
Low Voltage Indicator	✓
Power-On Clear	✓
Other Peripherals	Operational Amplifier 1 channel

Timers	
Timers (8-bit)	2 ch.
Timers (16-bit)	1 ch.
Interval timers	—
Special timers	—
Watchdog timers	1 ch.
PWM (8-bit resolution)	1 ch.