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UPD78F0550MA-FAA-AX

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Specification

Core	
CPU	78K0 (8-bit)
Main oscillator	10 MHz external, 8 MHz internal
Sub-oscillator	30 kHz internal
Clock supervising	—
Internal clock frequency (max.)	10 MHz
Instruction cycle time (min.)	100 ns
Interrupt channels	2 external, 10 internal

Memory	
Program memory	4 KB flash
RAM size	384 byte
Non-volatile memory size (EEPROM or emulation)	0 byte
ROM correction points	—

Interfaces	
Total number of I/Os	12
Open drain outputs	—
CAN	—
UART (asynchronous serial interface)	1 ch.
LIN	—
CSI (clocked serial interface)	—
I ² C	1 ch.
IEbus	—
On-chip debug interface	✓

Peripherals	
LCD controller/driver	—
A/D converter (10-bit)	4 ch.
Hardware multiplier	n/a
Hardware divider	n/a
Width of real-time output port	1 ch.
DMA	—
Low Voltage Indicator	✓✓
Power-On Clear	✓✓

Timers	
Timers (8-bit)	2 ch.
Timers (16-bit)	1 ch.
Interval timers	—
Special timers	—
Watchdog timers	1 ch.