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UPD70F3770GC-UEU-AX

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Specification

Core	
CPU	V850ES (32-bit)
Performance	98 MIPS
Main oscillator	48 MHz external
Sub-oscillator	32.768 kHz external, 220 kHz internal
Clock supervising	✓
Internal clock frequency (max.)	48 MHz
Instruction cycle time (min.)	20.8 ns
Interrupt channels	17 external, 73 internal

Memory	
Program memory	256 KB flash
RAM size	40 KB
Non-volatile memory size (EEPROM or emulation)	0 byte
ROM correction points	—
External address bus width	24 bit
External data bus width	16 bit

Interfaces	
Total number of I/Os	77
Open drain outputs	29
CAN	CAN: 1 ch.
UART (asynchronous serial interface)	5 ch.
LIN	5 ch.
CSI (clocked serial interface)	5 ch.
I ² C	3 ch.
IEbus	—
On-chip debug interface	✓

Peripherals	
LCD controller/driver	—
A/D converter (10-bit)	12 ch.
D/A converter (8-bit)	2 ch.
Hardware multiplier	16x16->32
Hardware divider	n/a
Width of real-time output port	1 ch.
DMA	4 ch.
Low Voltage Indicator	✓
Power-On Clear	—
Other Peripherals	USB Function 1ch

Timers	
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