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UPD70F3741GC-UEU-AX

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Specification

Core	
CPU	V850ES (32-bit)
Performance	69 MIPS
Main oscillator	10 MHz external
Sub-oscillator	32.768 kHz external, 220 kHz internal
Clock supervising	✓
Internal clock frequency (max.)	32 MHz
Instruction cycle time (min.)	31.25 ns
Interrupt channels	9 external, 48 internal

Memory	
Program memory	768 KB flash
RAM size	60 KB
Non-volatile memory size (EEPROM or emulation)	0 byte
ROM correction points	—
External address bus width	22 bit
External data bus width	16 bit

Interfaces	
Total number of I/Os	84
Open drain outputs	40
CAN	—
UART (asynchronous serial interface)	3 ch.
LIN	3 ch.
CSI (clocked serial interface)	5 ch.
I ² C	3 ch.
IEbus	—
On-chip debug interface	✓

Peripherals	
LCD controller/driver	—
A/D converter (10-bit)	12 ch.
D/A converter (8-bit)	2 ch.
Hardware multiplier	16x16->32,32x32->64
Hardware divider	n/a
Width of real-time output port	1 ch.
DMA	4 ch.
Low Voltage Indicator	✓
Power-On Clear	—
Other Peripherals	CRC

Timers	
Timers (8-bit)	—
Timers (16-bit)	8 ch.