

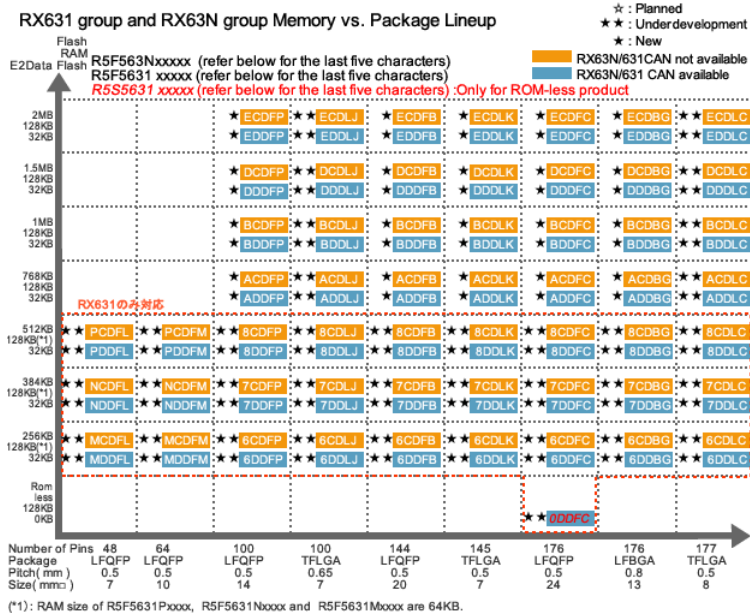


# RX63N, 631

## Overview of the RX63N/RX631 Group

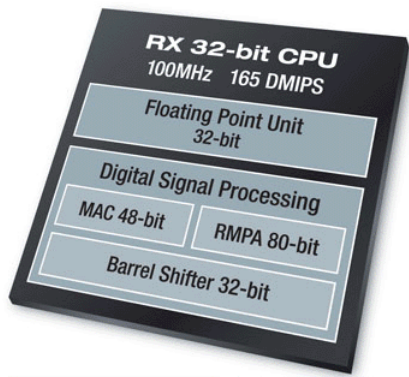
The RX63N/RX631 Group incorporates communication functions suitable for networking equipment, such as Ethernet controller, USB 2.0 full-speed (function, host, or OTG selectable), and CAN. In addition, with a RTC (Real-Time Clock) that can operate on a dedicated power supply as a low power feature, standby power consumption can be reduced by approximately 90% compared to existing products. The selection of on-chip memory has been expanded from ROMless to 2 MB, and even smaller-sized packages are available. This makes possible mounting in anything from large-scale systems to small-scale/small-space devices.

## Memory vs. Package Lineup



## RX63N/RX631 Group Block Diagrams

RX63N



**Memory**

- Zero-wait Flash up to 2MB
- SRAM up to 128KB
- Data Flash 32KB

**System**

- Data Management DTC/ExDMA/DMA
- Interrupt Cont. 16 levels 16 pins
- Clock Generation OSC PLL IRC
- POR/LVD

**Analog**

- ADC 12-bit 21 ch
- ADC 10-bit 8 ch
- DAC 10-bit 2 ch
- Temp Sensor

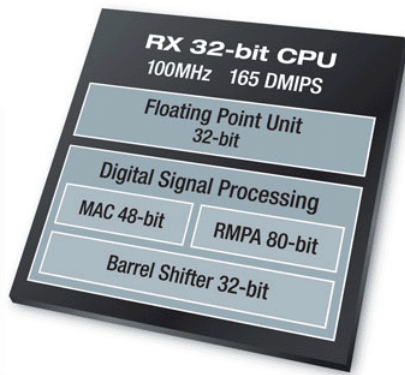
**Timers**

- MTU2 16-bit 6 ch
- 2 x TPU 16-bit 6 ch
- 2 x PPG 4-bit 4 gp
- 2 x TMR 8-bit 2 ch
- 2 x CMT 16-bit 2 ch
- WDT 8-bit 1 ch
- I-WDT 14-bit 1 ch
- RTC w/Vbat Calendar/Anti-tamper

**Communication**

- Ethernet 10/100 MAC with DMA
- 2 x USB 2.0 FS Host/Device/OTG
- 3 x CAN 2.0B
- 4 x I2C
- 13 x SCI
- 3 x SPI
- External Bus with SDRAM
- GPIO

RX631



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**Communication**

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- 3 x CAN 2.0B
- 4 x I<sup>2</sup>C
- 13 x SCI
- 3 x SPI
- External Bus with SDRAM
- GPIO

**RX63N/RX631 Group Specifications**

item	RX63N/RX631 Group
CPU core	RX CPU General registers: 32-bit x 16 Multiplier: 32-bit multiplier Divider: Yes Multiply-accumulator: Yes (two types: memory-to-memory operations and register-to-register operations)
Maximum operating frequency	100MHz
Power supply voltage	2.7 to 3.6V
Floating-point processing unit	Single-precision floating-point processing unit (Supports add/subtract/compare/multiply/divide and other instructions)
Flash ROM (for program storage)	Max. 2MB
Flash ROM (for data storage)	Max. 32KB
RAM	Max. 128KB
On-chip peripheral functions	External bus: 177/176-pin(32bit@50MHz), 145/144-pin (16bit@50MHz), Under 100-pin PKG (16bit@25MHz) DMAC: 4 channels Direct memory access controller designed exclusively (EXDMAC) x 2 channels:Over 100-pin PKG Only Data transfer controller (DTC) 16-bit multifunction timer pulse unit: 177/176/145/144-pin: 12 channels; Under 100-pin: 6 channels Multifunction timer pulse unit 2: 6 channels Port output enable 2 Programmable pulse generator: Max. 32-bit output 8-bit timer: 4 channels Compare match timer: 4 channels

	Real-time clock (RTC): Except 48-pin PKG Watchdog timer Independent watchdog timer Serial communication interface (SCIc): 177/176/145/144-pin: 12 channels; 100-pin: 8 channels; 64-pin: 5 channels; 48-pin:4 channels Serial communication interface (SCId) I <sup>2</sup> C bus interface: 177/176/145/144-pin:4 channels;100-pin:2 channels; 64/48-pin:1 channel Serial peripheral interface (RSPi): 177/176/145/144-pin:3 channels;Under 100-pin:2 channels USB 2.0 full-speed (Host/function/OTG): 177/176-pin: 2 channels; up to 145-pin: 1 channels Ethernet controller*1 DMA controller for Ethernet controller (EDMAC)*1 CAN*2 : Max. 3 channels IEBus x 1 ch (IEB) 12-bit A/D converter: 177/176/145/144-pin: 21 channels; 100-pin: 14 channels; 64-pin: 12 channels;48-pin: 8 channels 10-bit A/D converter: Over 100-pin;8 channels 10-bit D/A converter: 177/176/145/144-pin: 2 channels; 100/64-pin: 1channel Temperature sensor CRC calculator Power-on reset circuit (POR) Voltage detection circuit (LVD)
On-chip debugging function	Yes (with trace function)
Low power consumption modes	4 modes Sleep mode All-module clock stop mode Software standby mode Deep software standby mode
Packages	LQFP64, LQFP80, LGA85, LQFP100, LGA100, LQFP144, LGA145, BGA176, LQFP176, LGA177, LQFP48

\*1 RX63N Group (Over 100-pin PKG) only.

\*2 Part number differs between with CAN/without CAN versions.

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**Product Lineup:**

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167 matches











(1 to 100 are displayed.)


Items per page























1 2

Compare	Part No	Buy or Sample	Program Memory (KB)	RAM (KB)	Pin Count	Operating Voltage Min (V)	Production Status	Package Code
<input type="checkbox"/>	<a href="#">R5F5631BDDFB</a>	-	1024	128	144	2.7	Mass Production	PLQP0144KA -A (FP-144LV)
<input type="checkbox"/>	<a href="#">R5F5631BCDFB</a>	-	1024	128	144	2.7	Mass Production	PLQP0144KA -A (FP-144LV)
<input type="checkbox"/>	<a href="#">R5F5631NBDDFB</a>	-	1024	128	144	2.7	Mass Production	PLQP0144KA -A (FP-144LV)
<input type="checkbox"/>	<a href="#">R5F5631NBCDFB</a>	-	1024	128	144	2.7	Mass Production	PLQP0144KA -A (FP-144LV)
<input type="checkbox"/>	<a href="#">R5F5631NADDFB</a>	-	768	128	144	2.7	Mass Production	PLQP0144KA -A (FP-144LV)
<input type="checkbox"/>	<a href="#">R5F5631NACDFB</a>	-	768	128	144	2.7	Mass Production	PLQP0144KA -A (FP-144LV)
<input type="checkbox"/>	<a href="#">R5F5631PDDFM</a>	-	512	64	64	2.7	Under Development	PLQP0064KB -A (FP-64KV)
<input type="checkbox"/>	<a href="#">R5F5631NEDDFC</a>	-	2048	128	176	2.7	Mass Production	PLQP0176KB -A (FP-176EV)
<input type="checkbox"/>	<a href="#">R5F5631NECDFC</a>	-	2048	128	176	2.7	Mass Production	PLQP0176KB -A (FP-176EV)

<input type="checkbox"/>		<a href="#">R5F563NDDDFC</a>	-	1536	128	176	2.7	Mass Production	PLQP0176KB -A (FP-176EV)
<input type="checkbox"/>		<a href="#">R5F563NDCDFC</a>	-	1536	128	176	2.7	Mass Production	PLQP0176KB -A (FP-176EV)
<input type="checkbox"/>		<a href="#">R5F563NBDDFC</a>	-	1024	128	176	2.7	Mass Production	PLQP0176KB -A (FP-176EV)
<input type="checkbox"/>		<a href="#">R5F563NBCDFC</a>	-	1024	128	176	2.7	Mass Production	PLQP0176KB -A (FP-176EV)
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<input type="checkbox"/>		<a href="#">R5F563NACDFC</a>	-	768	128	176	2.7	Mass Production	PLQP0176KB -A (FP-176EV)
<input type="checkbox"/>		<a href="#">R5F5631EDDFC</a>	-	2048	128	176	2.7	Mass Production	PLQP0176KB -A (FP-176EV)
<input type="checkbox"/>		<a href="#">R5F5631ECDFC</a>	-	2048	128	176	2.7	Mass Production	PLQP0176KB -A (FP-176EV)
<input type="checkbox"/>		<a href="#">R5F5631DDDFC</a>	-	1536	128	176	2.7	Mass Production	PLQP0176KB -A (FP-176EV)
<input type="checkbox"/>		<a href="#">R5F5631DCDFC</a>	-	1536	128	176	2.7	Mass Production	PLQP0176KB -A (FP-176EV)
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<input type="checkbox"/>		<a href="#">R5F5631BCDFC</a>	-	1024	128	176	2.7	Mass Production	PLQP0176KB -A (FP-176EV)
<input type="checkbox"/>		<a href="#">R5F5631ADDFC</a>	-	768	128	176	2.7	Mass Production	PLQP0176KB -A (FP-176EV)
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<input type="checkbox"/>		<a href="#">R5F563NEDDFB</a>	-	2048	128	144	2.7	Mass Production	PLQP0144KA -A (FP-144LV)
<input type="checkbox"/>		<a href="#">R5F563NECDFB</a>	-	2048	128	144	2.7	Mass Production	PLQP0144KA -A (FP-144LV)
<input type="checkbox"/>		<a href="#">R5F563NDDDFB</a>	-	1536	128	144	2.7	Mass Production	PLQP0144KA -A (FP-144LV)
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<input type="checkbox"/>		<a href="#">R5F5631ADDFB</a>	-	768	128	144	2.7	Mass Production	PLQP0144KA -A (FP-144LV)

<input type="checkbox"/>		<a href="#">R5F5631ACDFB</a>	-	768	128	144	2.7	Mass Production	<a href="#">PLQP0144KA</a> -A (FP-144LV)
<input type="checkbox"/>		<a href="#">R5F5631DDDFP</a>	-	1536	128	100	2.7	Mass Production	<a href="#">PLQP0100KB</a> -A (FP-100UV)
<input type="checkbox"/>		<a href="#">R5F5631BDDFP</a>	-	1024	128	100	2.7	Mass Production	<a href="#">PLQP0100KB</a> -A (FP-100UV)
<input type="checkbox"/>		<a href="#">R5F5631BCDFP</a>	-	1024	128	100	2.7	Mass Production	<a href="#">PLQP0100KB</a> -A (FP-100UV)
<input type="checkbox"/>		<a href="#">R5F5631NADDFP</a>	-	768	128	100	2.7	Mass Production	<a href="#">PLQP0100KB</a> -A (FP-100UV)
<input type="checkbox"/>		<a href="#">R5F5631ADDFP</a>	-	768	128	100	2.7	Mass Production	<a href="#">PLQP0100KB</a> -A (FP-100UV)
<input type="checkbox"/>		<a href="#">R5F5631ACDFP</a>	-	768	128	100	2.7	Mass Production	<a href="#">PLQP0100KB</a> -A (FP-100UV)
<input type="checkbox"/>		<a href="#">R5F5631NACDFP</a>	-	768	128	100	2.7	Mass Production	<a href="#">PLQP0100KB</a> -A (FP-100UV)
<input type="checkbox"/>		<a href="#">R5F5631EDDFP</a>	-	2048	128	100	2.7	Mass Production	<a href="#">PLQP0100KB</a> -A (FP-100UV)
<input type="checkbox"/>		<a href="#">R5F5631ECDFP</a>	-	2048	128	100	2.7	Mass Production	<a href="#">PLQP0100KB</a> -A (FP-100UV)
<input type="checkbox"/>		<a href="#">R5F5631DCDFP</a>	-	1536	128	100	2.7	Mass Production	<a href="#">PLQP0100KB</a> -A (FP-100UV)
<input type="checkbox"/>		<a href="#">R5F5631NEDDFP</a>	-	2048	128	100	2.7	Mass Production	<a href="#">PLQP0100KB</a> -A (FP-100UV)
<input type="checkbox"/>		<a href="#">R5F5631NECDFP</a>	-	2048	128	100	2.7	Mass Production	<a href="#">PLQP0100KB</a> -A (FP-100UV)
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<input type="checkbox"/>		<a href="#">R5F5631NBCDFP</a>	-	1024	128	100	2.7	Mass Production	<a href="#">PLQP0100KB</a> -A (FP-100UV)
<input type="checkbox"/>		<a href="#">R5F5631EDDBG</a>	-	2048	128	176	2.7	Mass Production	<a href="#">PLBG0176GA</a> -A (BP-176V)
<input type="checkbox"/>		<a href="#">R5F5631NADDBG</a>	-	768	128	176	2.7	Mass Production	<a href="#">PLBG0176GA</a> -A (BP-176V)
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<input type="checkbox"/>		<a href="#">R5F5631NECDBG</a>	-	2048	128	176	2.7	Mass Production	<a href="#">PLBG0176GA</a> -A (BP-176V)
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<input type="checkbox"/>		<a href="#">R5F563NDCDBG</a>	-	1536	128	176	2.7	Mass Production	PLBG0176GA -A (BP-176V)
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<input type="checkbox"/>		<a href="#">R5F563NBCDBG</a>	-	1024	128	176	2.7	Mass Production	PLBG0176GA -A (BP-176V)
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<input type="checkbox"/>		<a href="#">R5F5631DDDBG</a>	-	1536	128	176	2.7	Mass Production	PLBG0176GA -A (BP-176V)
<input type="checkbox"/>		<a href="#">R5F5631DCDBG</a>	-	1536	128	176	2.7	Mass Production	PLBG0176GA -A (BP-176V)
<input type="checkbox"/>		<a href="#">R5F5631BDDBG</a>	-	1024	128	176	2.7	Mass Production	PLBG0176GA -A (BP-176V)
<input type="checkbox"/>		<a href="#">R5F5631BCDBG</a>	-	1024	128	176	2.7	Mass Production	PLBG0176GA -A (BP-176V)
<input type="checkbox"/>		<a href="#">R5F5631ADDBG</a>	-	768	128	176	2.7	Mass Production	PLBG0176GA -A (BP-176V)
<input type="checkbox"/>		<a href="#">R5F5631ACDBG</a>	-	768	128	176	2.7	Mass Production	PLBG0176GA -A (BP-176V)
<input type="checkbox"/>		<a href="#">R5F5631PCDFL</a>	-	512	64	48	2.7	Under Development	PLQP0048KB -A (48P6Q-A)
<input type="checkbox"/>		<a href="#">R5F5631PDDFL</a>	-	512	64	48	2.7	Under Development	PLQP0048KB -A (48P6Q-A)
<input type="checkbox"/>		<a href="#">R5F5631NCDFL</a>	-	384	64	48	2.7	Under Development	PLQP0048KB -A (48P6Q-A)
<input type="checkbox"/>		<a href="#">R5F5631NDDFL</a>	-	384	64	48	2.7	Under Development	PLQP0048KB -A (48P6Q-A)
<input type="checkbox"/>		<a href="#">R5F5631MCDFL</a>	-	256	64	48	2.7	Under Development	PLQP0048KB -A (48P6Q-A)
<input type="checkbox"/>		<a href="#">R5F5631MDDFL</a>	-	256	64	48	2.7	Under Development	PLQP0048KB -A (48P6Q-A)
<input type="checkbox"/>		<a href="#">R5F5631PCDFM</a>	-	512	64	64	2.7	Under Development	PLQP0064KB -A (FP-64KV)
<input type="checkbox"/>		<a href="#">R5F5631NCDFM</a>	-	384	64	64	2.7	Under Development	PLQP0064KB -A (FP-64KV)
<input type="checkbox"/>		<a href="#">R5F5631NDDFM</a>	-	384	64	64	2.7	Under Development	PLQP0064KB -A (FP-64KV)
<input type="checkbox"/>		<a href="#">R5F5631MCDFM</a>	-	256	64	64	2.7	Under Development	PLQP0064KB -A (FP-64KV)
<input type="checkbox"/>		<a href="#">R5F5631MDDFM</a>	-	256	64	64	2.7	Under Development	PLQP0064KB -A (FP-64KV)
<input type="checkbox"/>		<a href="#">R5S56310DDFC</a>	-	0	128	176	2.7	Under Development	PLQP0176KB -A (FP-176EV)
<input type="checkbox"/>		<a href="#">R5F5631ECDLJ</a>	-	2048	128	100	2.7	Under Development	PTLG0100JA- A (100F0G)

<input type="checkbox"/>		R5F5631EDDLJ	-	2048	128	100	2.7	Under Development	PTLG0100JA-A (100F0G)
<input type="checkbox"/>		R5F5631DCDLJ	-	1536	128	100	2.7	Under Development	PTLG0100JA-A (100F0G)
<input type="checkbox"/>		R5F5631DDDLJ	-	1536	128	100	2.7	Under Development	PTLG0100JA-A (100F0G)
<input type="checkbox"/>		R5F5631BCDLJ	-	1024	128	100	2.7	Under Development	PTLG0100JA-A (100F0G)
<input type="checkbox"/>		R5F5631BDDLJ	-	1024	128	100	2.7	Under Development	PTLG0100JA-A (100F0G)
<input type="checkbox"/>		R5F5631NECDLJ	-	2048	128	100	2.7	Under Development	PLQP0100KB -A (FP-100UV)
<input type="checkbox"/>		R5F5631NEDDLJ	-	2048	128	100	2.7	Under Development	PLQP0100KB -A (FP-100UV)
<input type="checkbox"/>		R5F5631NCDLJ	-	1536	128	100	2.7	New Products	PLQP0100KB -A (FP-100UV)
<input type="checkbox"/>		R5F5631NDDDLJ	-	1536	128	100	2.7	New Products	PLQP0100KB -A (FP-100UV)
<input type="checkbox"/>		R5F5631NBCDLJ	-	1024	128	100	2.7	Under Development	PLQP0100KB -A (FP-100UV)
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<input type="checkbox"/>		R5F5631ACDLK	-	768	128	145	2.7	Mass Production	PTLG0145KA -A (145F0G)
<input type="checkbox"/>		R5F5631ADDLK	-	768	128	145	2.7	Mass Production	PTLG0145KA -A (145F0G)
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