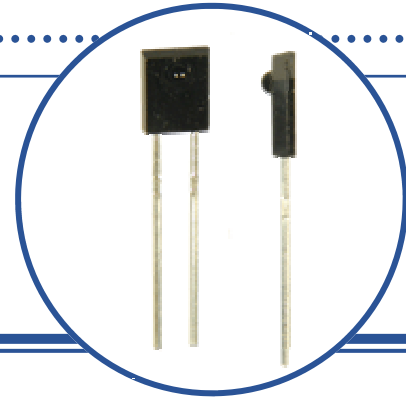


NPN Silicon Phototransistors in Side Looking Package

OP552A, OP552B, OP552C

- Wide receiving angle
- Four sensitivity ranges
- Side-looking package for space limited applications
- Opaque epoxy package for visible lighting immunity



The OP552 series are silicon phototransistors mounted in opaque epoxy packages. The package is optically transparent to infrared light, but is opaque to visible wavelengths. This allows the device to be used in high ambient light conditions or anywhere external light sources could interfere with the intended sensing application.

The device incorporates an integral molded lens which enables a wide receiving angle and provides even reception over a large area. The side looking package is designed for PC board mounting and enables sensing in a plane perpendicular to the mounting surface. The OP552 series is mechanically and spectrally matched to the OP141 series infrared emitting diodes.

Applications

- Non-Contact Position Sensing
- Datum detection
- Machine automation
- Optical encoders

Ordering Information

| Part Number | On-State Collector Current Range |
|-------------|----------------------------------|
| OP552A | 2.55 mA— |
| OP552B | 1.30 mA—4.70 mA |
| OP552C | 0.25 mA—2.40 mA |
| OP552D | 0.25 mA— |

Absolute Maximum Ratings

$T_A = 25^\circ\text{C}$ unless otherwise noted

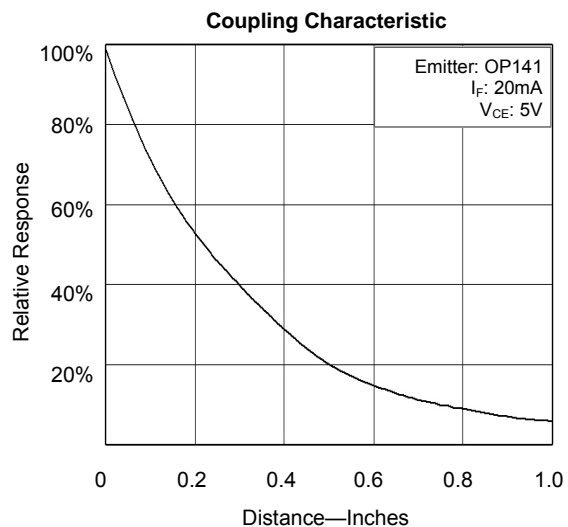
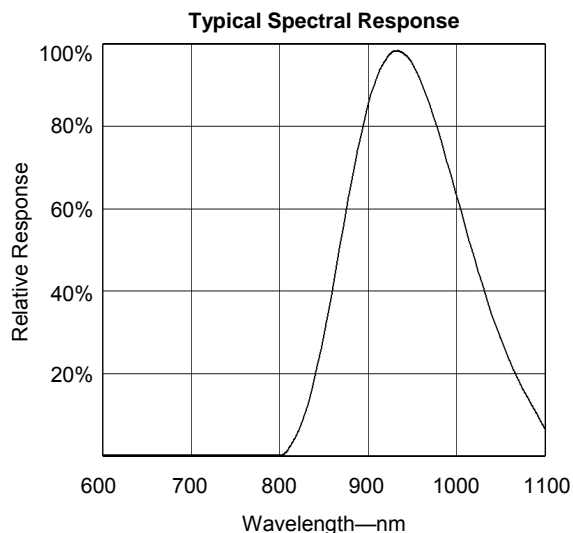
| | |
|--|-----------------------|
| Storage Temperature Range | -40° C to +100° C |
| Operating Temperature Range | -40° C to +85° C |
| Lead Soldering Temperature [1/16 inch (1.6mm) from case for 5 sec with soldering iron] | 260° C ⁽¹⁾ |
| Collector—Emitter Voltage | 30 V |
| Emitter—Collector voltage | 5 V |
| Power Dissipation | 100 mW ⁽²⁾ |

Notes:

1. RMA flux is recommended. Duration can be extended to 10 sec when flow soldering.
2. De-rate linearly at 1.25 mW/° C above 25° C.
3. Light source is an unfiltered GaAl LED with a peak emission wavelength of 935nm.
4. To Calculate typical collector dark current in μA , use the formula $I_{CEO} = 10^{(0.04 T_A - 3/4)}$ where T_A is the ambient temperature in ° C.

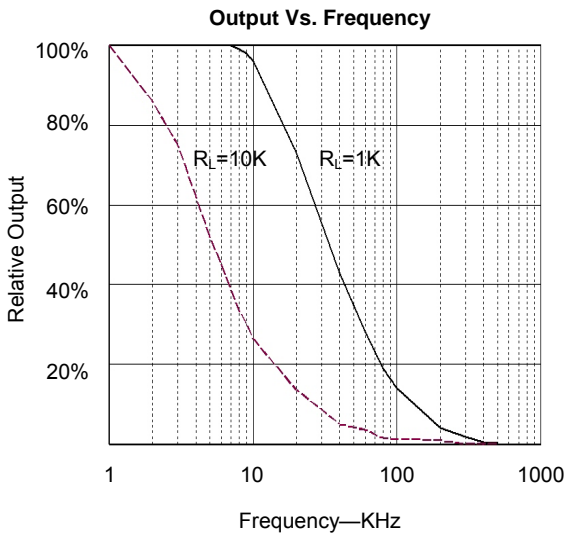
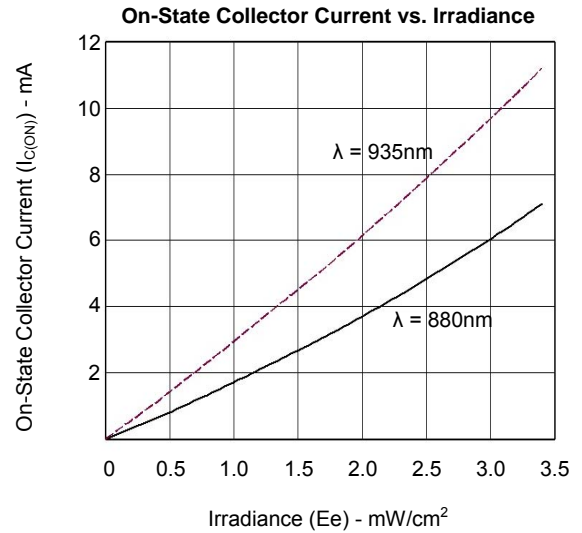
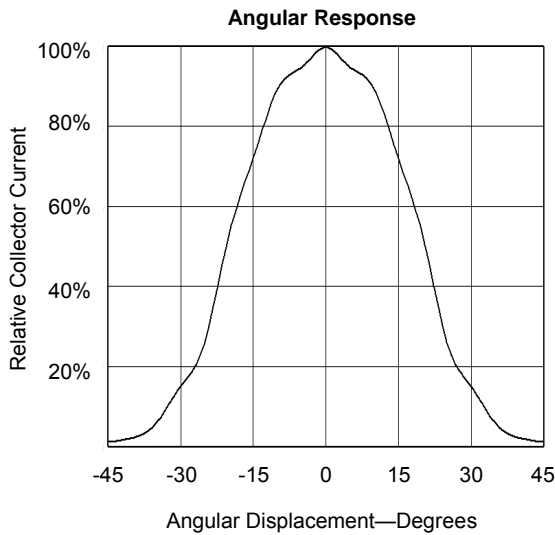
Electrical Characteristics ($T_A = 25^\circ\text{C}$ unless otherwise noted)

| SYMBOL | PARAMETER | MIN | MAX | UNITS | CONDITIONS | |
|---------------|--------------------------------------|--------|------|-------|---|---|
| $I_{C(ON)}$ | On-State Collector Current | OP552D | 0.25 | - | mA | $V_{CE} = 5.0\text{V}$, $E_e = 1.0\text{mW/cm}^2$ (note 3) |
| | | OP552C | 0.25 | 2.40 | | |
| | | OP552B | 1.30 | 4.70 | | |
| | | OP552A | 2.55 | - | | |
| $V_{CE(SAT)}$ | Collector-Emitter Saturation Voltage | | 0.4 | V | $I_C = 100\mu\text{A}$, $E_e = 1.0\text{mW/cm}^2$ (Note 3) | |
| I_{CEO} | Collector-Emitter Dark Current | | 100 | nA | $V_{CE} = 5.0\text{V}$, $E_e = 0$ (Note 4) | |
| $V_{(BR)CEO}$ | Collector-Emitter Breakdown Voltage | 30 | | V | $I_C = 100\mu\text{A}$ | |
| $V_{(BR)ECO}$ | Emitter-Collector Breakdown Voltage | 5 | | V | $I_E = 100\mu\text{A}$ | |

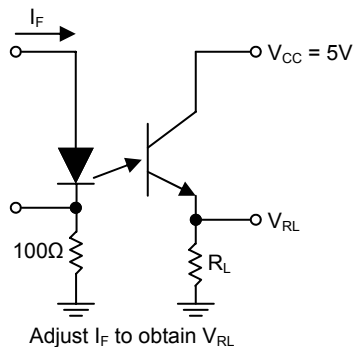


Side Looking Silicon Phototransistors

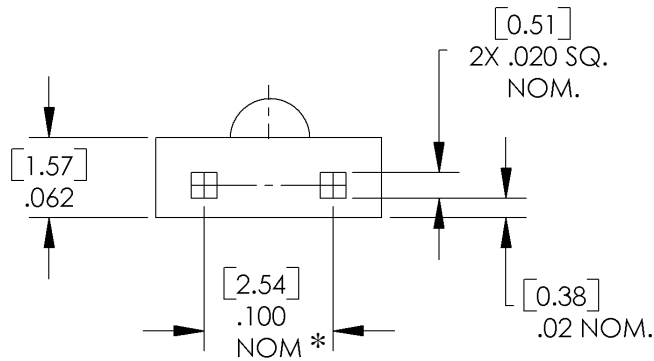
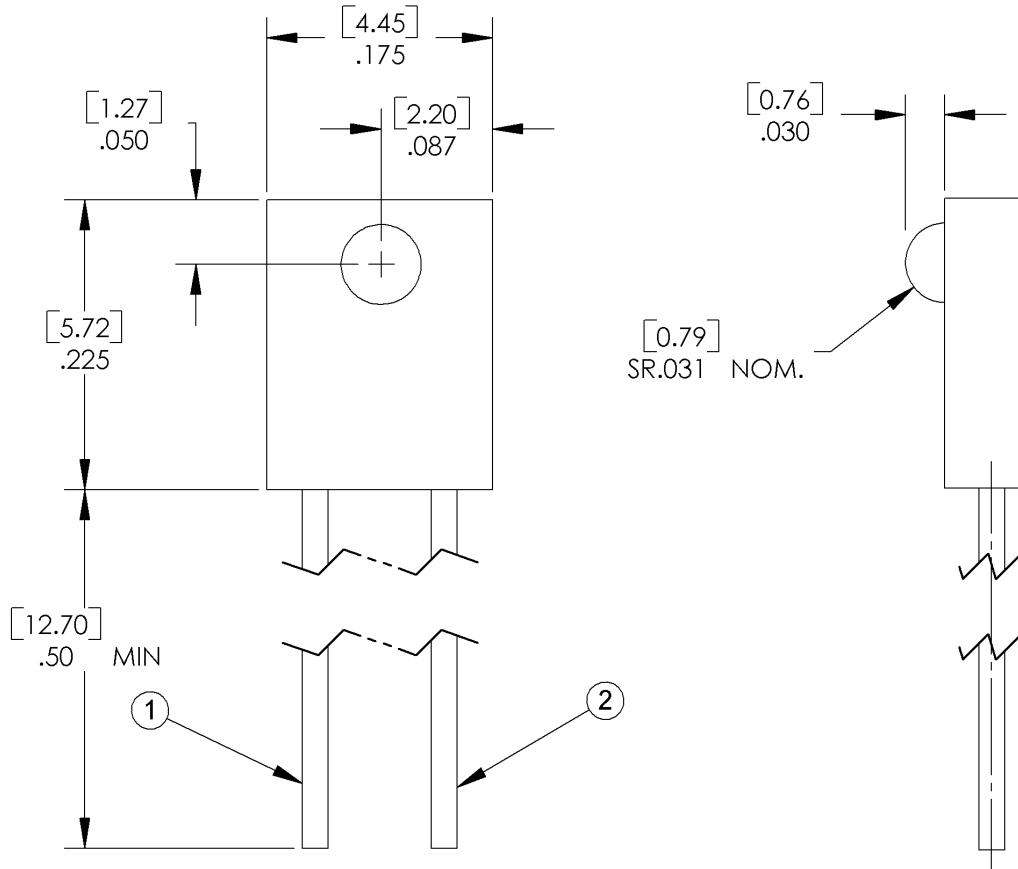
OP552 Series



Switching Test Circuit



Mechanical Data



DISCRETE PIN-OUT

- 1 - EMITTER
- 2 - COLLECTOR

Notes:

1. All dimensions are in inches [mm]
2. Tolerances are $\pm 0.005''$ [0.13m] unless otherwise stated
3. Mold flash at lead egress and body edges
4. * = measured at lead egress from body