

Quick Start Guide TRK-KEA

Kinetis EA Series MCUs for Automotive Electronics Applications



Quick Start Guide

GET TO KNOW THE TRK-KEA

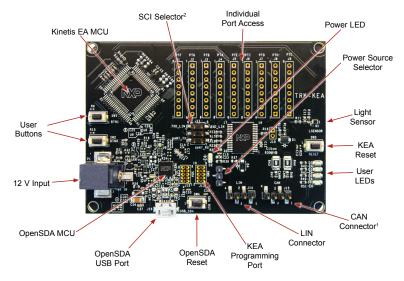


Figure 1: Front side of TRK-KEA

INTRODUCTION AND DEFAULT SETTINGS

The TRK-KEA features either one of the KEA128, KEA64 or KEA8 MCUs, depending on the board version. The KEA family is aimed at automotive body electronic applications.

Kinetis EA series MCUs integrate an Arm® Cortex®-M0+ core, a CAN module³, a UART module with LIN capabilities, a pulse width timer (PWT) and a keyboard interrupt module (KBI). All these peripherals together with standard serial communication protocols such as I2C and SPI offer flexibility for a width variety of applications.

The TRK-KEA board includes an onboard OpenSDA programmer and debugger, LIN physical transceiver, CAN physical transceiver, a light sensor, four LEDs and two pushbuttons for user interface.

This guide will show how to connect the board to a host PC and execute a demonstration application preloaded into the flash memory. Default jumper positions of the TRK-KEA board are shown in the figure.

SOFTWARE TOOLS INSTALLATION

1 Install CodeWarrior Development Studio

NXP's Codewarrior for MCUs integrates several functionalities into a rapid development software bundle. It features support for Arm architecture code development and compilation in a friendly user interface. It also features a powerful debugging tool that can support various debugging tools including the low cost OpenSDA interface. Along with this, Codewarrior includes the Processor Expert tool. This tool allows to create, configure, optimize, migrate, and deliver software components that generate source code for NXP silicon quickly and easily.

The latest version of CodeWarrior for MCUs (Eclipse IDE) can be downloaded from <u>www.nxp.com/CodeWarrior</u>.

Launch the Demo Program

The DVD contains CodeWarrior projects to exercise the different modules of the Kinetis EA series MCU, including the UART, FTM, ADC and keyboard interrupts (KBI) modules.

JUMPER DEFAULT CONFIGURATION

JUMPER	SETTING	DESCRIPTION	
J10*	1-3, 2-4	UART to OpenSDA serial selection	
J13	1-2	Power selection, USB enabled	

*J10 only available on TRK-KEA8

JUMPER LIST AND DESCRIPTION

JUMPER	DESCRIPTION		
	SCI Selector		
	Pin 1-3 closed: UART to OpenSDA Rx		
J10	Pin 5-3 closed: UART to LIN Rx		
	Pin 2-4 closed: UART to OpenSDA Tx		
	Pin 6-4 closed: UART to LIN Tx		
	Power Source Selector		
J13	Pins 1-2 closed: USB source		
	Pin 3-2 closed: External source		

HEADERS AND CONNECTORS LIST

HEADER/ CONNECTOR	DESCRIPTION	
J1	User access port F	
J2	User access port G	
J3	User access port B	
J4	User access port A	
J5	User access port E	
J6	User access port I	
J7	User access port D	
J8	User access port H	
J9	User access port C	
J11	SDA port (external program and debug interface)—OpenSDA MCU	
J12	SWD port (external program and debug interface)—Main MCU	
J14	LIN connector	
J15*	CAN connector	

*J15 only available on TRK-KEA128

PERIPHERAL LIST

PERIPHERAL	ID	MCU PORT	DESCRIPTION
Light sensor	Q1	ADC0_SE3	Light sensor connected to ADC channel 3
LED	D3	C0	Blue led connected to port C0
	D5	C1	Blue led connected to port C1
	D6	C2	Blue led connected to port C2
	D8	C3	Blue led connected to port C3
Button	SW1*	D0/C4	Switch connected to port D0/C4
	SW2**	D1/C5	Switch connected to port D1/C5

* SW1 connected to port D0 for TRK-KEA128 and TRK-KEA64. SW1 connected to port C4 for TRK-KEA8

** SW2 connected to port D1 for TRK-KEA128 and TRK-KEA64. SW2 connected to port C5 for TRK-KEA8

SUPPORT

Visit **www.nxp.com/support** for a list of phone numbers within your region.

WARRANTY

Visit **www.nxp.com/warranty** for complete warranty information.

HOME PAGE

Visit **www.nxp.com/TRK-KEA** for more information.

Get Started Download installation software and documentation under "Get Started" at www.nxp.com/TRK-KEA128.

www.nxp.com/TRK-KEA

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. Arm and Cortex are registered trademarks of Arm Limited (or its subsidiaries) in the EU and/or elsewhere. All rights reserved. © 2019 NXP B.V.

Doc Number: KINTESMCUAUTQSG REV 1