# CPC1945Y AC Solid State Relay 

|  | CPC1945Y | Units |
| :--- | :---: | :---: |
| Load Voltage | 120 | $V_{\text {RMS }}$ |
| Load Current | 1.0 | A |
| On-State <br> Voltage Drop | 1.6 | $\mathrm{V}_{\text {RMS }}$ <br> (at $\left.\mathrm{I}_{\mathrm{L}}=1.0 \mathrm{~A}\right)$ |

## Features

- Load Current up to 1A
- Blocking Voltage to 400 V
- 5mA Sensitivity
- Zero-Crossing Detection
- DC Control, AC Output
- Optically Isolated
- TTL and CMOS Compatible
- Low EMI and RFI Generation
- High Noise Immunity
- Machine Insertable, Wave Solderable
- Flammability classification rating of V - 0


## Applications

- Programmable Control
- Process Control
- Power Control Panels
- Remote Switching
- Gas Pump Electronics
- Contractors
- Large Relays
- Solenoids
- Motors
- Heaters


## Description

The CPC1945Y is a AC Solid State Switch using patented waveguide coupling with dual power SCR outputs to produce an alternative to optocoupler and Triac circuits. The CPC1945Y switches are robust enough to provide a blocking voltage of up to 400 V . In addition, tightly controlled zero cross circuitry ensures switching of AC loads without the generation of transients. The input and output circuits are optically coupled to provide 3750 V of isolation and noise immunity between control and load circuits. As a result the CPC1945Y is well suited for industrial environments where electromagnetic interference would disrupt the operation of electromechanical relays.

Approvals

- UL recognized to UL508, file \#: E69938
- CSA certified to CSA 14, file \#: LR43639

Ordering Information

| Part \# | Description |
| :--- | :--- |
| CPC1945Y | 8 Pin SIP (25/tube) |

## Pin Configuration

## CPC1945Y



Absolute Maximum Ratings (@ $25^{\circ} \mathrm{C}$ )

| Parameter | Min | Typ | Max | Units |
| :--- | :---: | :---: | :---: | :---: |
| Input Power Dissipation | - | - | $150^{1}$ | mW |
| Input Control Current | - | - | 100 | mA |
| Peak (10ms) | - | - | 1 | A |
| Reverse Input Voltage | - | - | 5 | V |
| Total Package Dissipation <br> PD | - | - | $1600^{2}$ | mW |
| Isolation Voltage <br> Input to Output | 3750 | - | - | $\mathrm{V}_{\text {RMS }}$ |
| Operational Temperature | -40 | - | +85 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature | -40 | - | +125 | ${ }^{\circ} \mathrm{C}$ |
| Soldering Temperature <br> (10 Seconds Max.) <br> DIP Package |  |  |  |  |

${ }^{1}$ Derate Linearly $1.33 \mathrm{~mW} / \mathrm{C}$
${ }^{2}$ Derate Linearly $16.6 \mathrm{~mW} / \mathrm{C}$

Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at conditions beyond those indicated in the operational sections of this data sheet is not implied.

## Electrical Characteristics

| Parameter | Conditions | Symbol | Min | Typ | Max | Units |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Output Characteristics @ $25^{\circ} \mathrm{C}$ |  |  |  |  |  |  |
| Operating Voltage Range | $V_{T}$ |  | 20 | - | 120 | $V_{\text {RMS }}$ |
| Peak Blocking Voltage | - | $V_{\text {DRM }}$ | - |  | 400 | V |
| Load Current (Continuous) | $\mathrm{V}_{\mathrm{L}}=120 \mathrm{VAC}$ | $\mathrm{I}_{\mathrm{L}}$ | 0.005 | - | 1.0 | $\mathrm{A}_{\text {RMS }}$ |
| Non-repetitive Single Cycle Surge Current | - | $\mathrm{I}_{\text {TSM }}$ | - | - | 10 | A |
| Off State Leakage Current | $V_{\text {DRM }}$ | $\mathrm{I}_{\text {LEAK }}$ | - | - | 1 | mA |
| On-State Voltage Drop | $\mathrm{I}_{\mathrm{L}}=1.0 \mathrm{~A}$ |  | - | - | 1.6 | $\mathrm{V}_{\text {RMS }}$ |
| Critical Rate of Rise ${ }^{3}$ |  | dv/dt | 1000 | - | - | V//us |
| Switching Speeds Turn-on | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}$ | $\mathrm{T}_{\text {ON }}$ | - | - | 0.5 | Cycles |
| Turn-off | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}$ | $\mathrm{T}_{\text {OFF }}$ | - | - | 0.5 | Cycles |
| Zero-Cross Turn-On Voltage Sub. half cycle | 1st half cycle | - |  | $\begin{aligned} & 2 \\ & 1 \end{aligned}$ | $\begin{aligned} & 10 \\ & \mathrm{~V} \end{aligned}$ | V |
| Operating Frequency ${ }^{1}$ | - |  | 20 | - | 400 | Hz |
| Load Power Factor for Guaranteed Turn-On² | - | PF | 0.25 | - | - | - |
| Capacitance Input to Output | - | - | - | 3 | - | pF |
| Input Characteristics @ $25^{\circ} \mathrm{C}$ |  |  |  |  |  |  |
| Input Control Current For Normal Environment | - | $I_{\text {F }}$ | 5 | - | 50 | mA |
| For High Noise Environment | - | $I_{\text {F }}$ | 10 | - | 100 | mA |
| Input Voltage Drop | $\mathrm{I}_{\mathrm{F}}=5 \mathrm{~mA}$ | $V_{F}$ | 0.9 | 1.2 | 1.4 | V |
| Input Drop-out Voltage | - |  | 0.8 | - | - | V |
| Reverse Input Current | $\mathrm{V}_{\mathrm{R}}=5 \mathrm{~V}$ | $\mathrm{I}_{\text {R }}$ | - | - | 10 | $\mu \mathrm{A}$ |

${ }^{1}$ Zero cross 1st $1 / 2$ cycle @ $<100 \mathrm{~Hz}$
${ }^{2}$ Snubber circuits may be required at low power factors.
${ }^{3}$ Tested in accordance with EIA/NARM Standard RS-443.

## PERFORMANCE DATA*

CPC1945Y
Typical LED Forward Voltage Drop (Ambient Temperature $=25^{\circ} \mathrm{C}$ )


CPC1945Y


CPC1945Y


CPC1945Y
Maximum Surge Current (non-repetitive)


CPC1945Y
Typical On-State Output Forward Voltage Distribution (Ambient Temperature $=\mathbf{2 5}{ }^{\circ} \mathrm{C}$ )


CPC1945Y
Typical Maximum Load Current vs. Temperature


CPC1945Y
Typical Output $\mathbf{V}_{\mathrm{F}}$ vs. Temperature


CPC1945Y
Typical Blocking Voltage Distribution (Ambient Temperature $=25^{\circ} \mathrm{C}$ )


CPC1945Y
Typical Leakage vs. Temperature Typical Leakage vs. Temperature
@ Maximum Rated Load Voltage


CPC1945Y
Typical LED Forward Voltage Drop


CPC1945Y
Typical $I_{F}$ for Switch Operation


[^0]
## MECHANICAL DIMENSIONS



Note:

1. Pin location tolerances are non-accumulative.

CLARE at any time without notice. Neither circuit patent licenses nor indemnity are expressed or implied. Except as set forth in Clare's Standard Terms and Conditions of Sale, Clare, Inc. assumes no liability whatsoever, and disclaims any express or implied warranty, relating to its products including, but not limited to, the implied warranty of merchantability, fitness for a particular purpose, or infringement of any intellectual property right.

The products described in this document are not designed, intended, authorized or warranted for use as components in systems intended for surgical implant into the body, or in other applications intended to support or sustain life, or where malfunction of Clare's product may result in direct physical harm, injury, or death to a person or severe property or environmental damage. Clare, Inc. reserves the right to discontinue or make changes to its products at any time without notice.


[^0]:    * The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

