

UPG2118K-EV24 Evaluation Board

Description:

The uPG2118K-EV24 evaluation board provides a convenient means of evaluating the performance of the uPG2118K at 2.4GHz.

Gain, output power, efficiency and linearity can be adjusted to a certain extent through the biasing conditions and matching networks.

Pin Designation and Connection

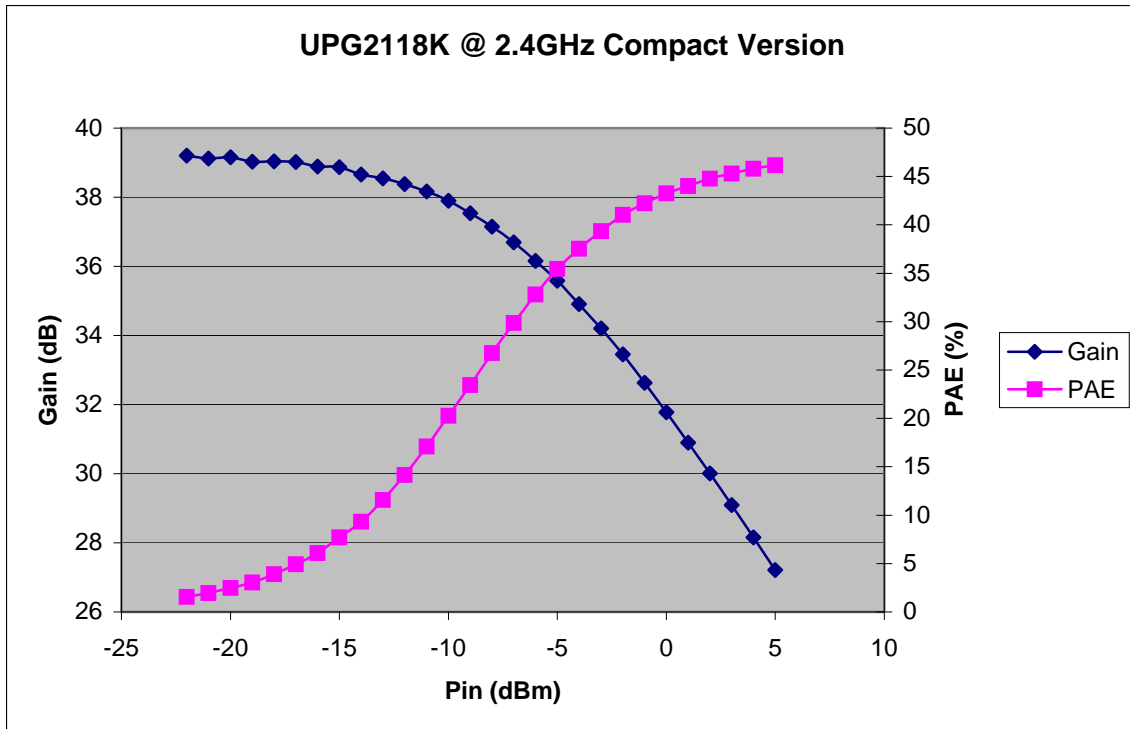
Vd1, Vd2, Vd3 and Vabc are connected together and designated as Vd and Vabc.
VGG1,2, VGG3, VATT and are together and designated as Vref.

Turn-on / Turn-off Procedure

This part is generally not sensitive to the turn-on and turn-off sequence as long as the maximum ratings are not exceeded. If users have any concerns regarding this issue consult with CEL.

Test Conditions & Results

Test conditions: $f=2.4\text{GHz}$, $V_d, V_{abc}=3.2\text{V}$, $V_{ref}=2.8\text{V}$



Other

Board produced by Network PCB @NetWorkPCB.com. Board material GETEK .028 mils, $\epsilon_r=4.2$.