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1 279 C04 247 Translation into english

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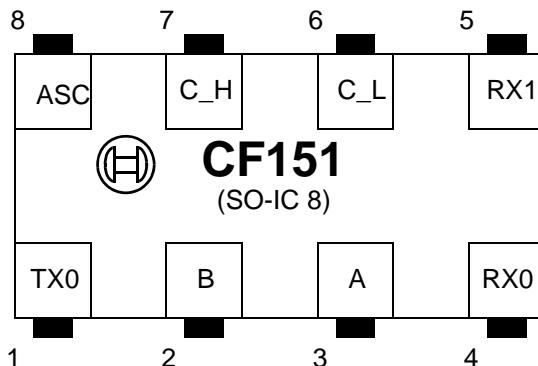
1 039 R00 726 correction on page 12/17 (Switch-off delay ...)

12.08.2010

1 039 R00 760 Revision: refence to part number deleted

09/01/10

## CAN - Transceiver CF151 (UB62)



## 1. Brief description

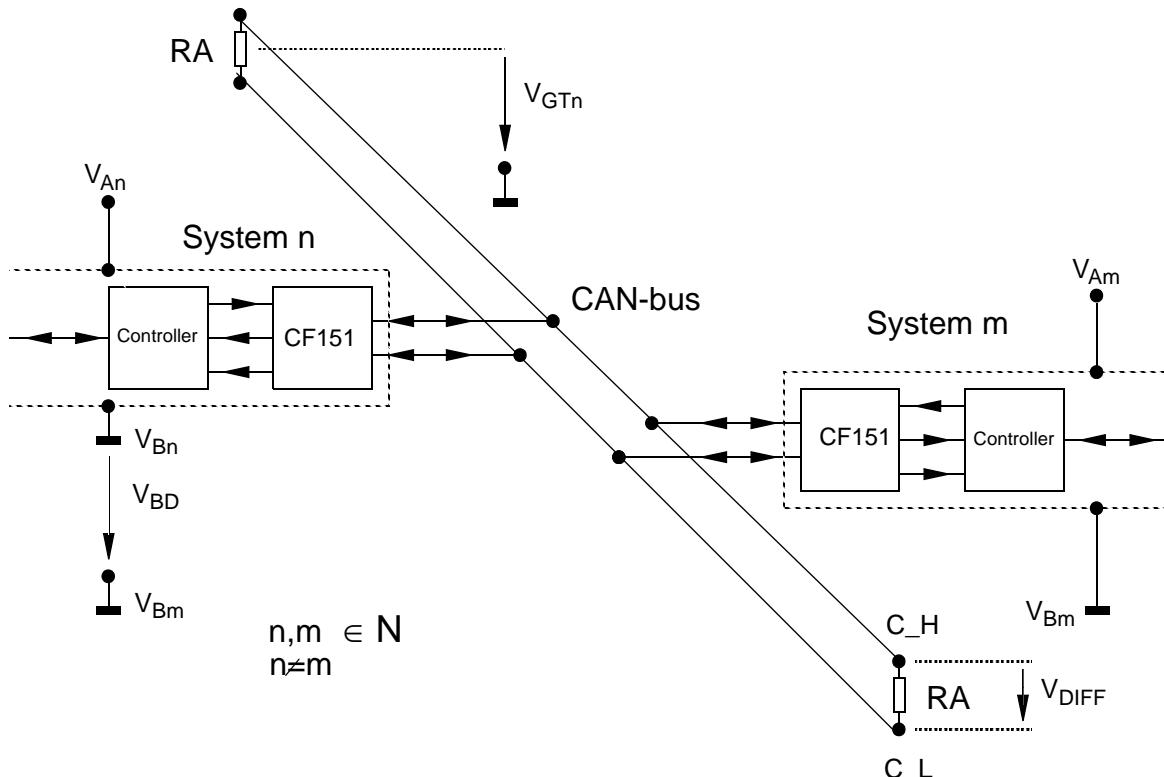
The CF151 is a bidirectional transceiver module for signal adaptation and processing between a CAN controller component and a CAN-bus two-wire line for data transmission rates up to 500 KBAUD.

The component consists of a transmitter which converts a digital signal from the controller component into a difference signal onto the CAN-bus. A receiver converts a common mode difference signal from the CAN bus into a digital signal to the CAN controller. A monitoring unit protects the component against destruction in the case of short-circuits and overvoltages on the CAN-bus for vehicle application.

The difference signal is built up with a limited rise time rate to suppress reflections and to minimise the radiation in unshielded bus systems. The rise time rate can be set externally to a fast (<500Kbaud) or slow mode (<125Kbaud).

The component is defined based on the standard ISO/DIS 11898 .

### 1.1 Function circuit diagram CF151



## 1.2 Properties of the transmitter output stage

|  | Block diagram              |
|--|----------------------------|
| - CMOS level input   | TX0                        |
| - High and low side switches in MOS technology for difference signal formation           | TH, TL                     |
| - $60\Omega$ Generator internal resistance for wave resistance adaptation to the CAN-bus | RG                         |
| - voltage-proof in the range from -5V to 36V   | C_H, C_L, DH, DL           |
| - high-ohmic voltage regulation to the CAN bus in recessive condition                    | $V_0, R_I$                 |
| - data transmission rate up to 500 KBAUD   | $TX0 \rightarrow C_H, C_L$ |
| - adjustable limited rise time rate  | ASC                        |

## 1.3 Properties of the receiver stage

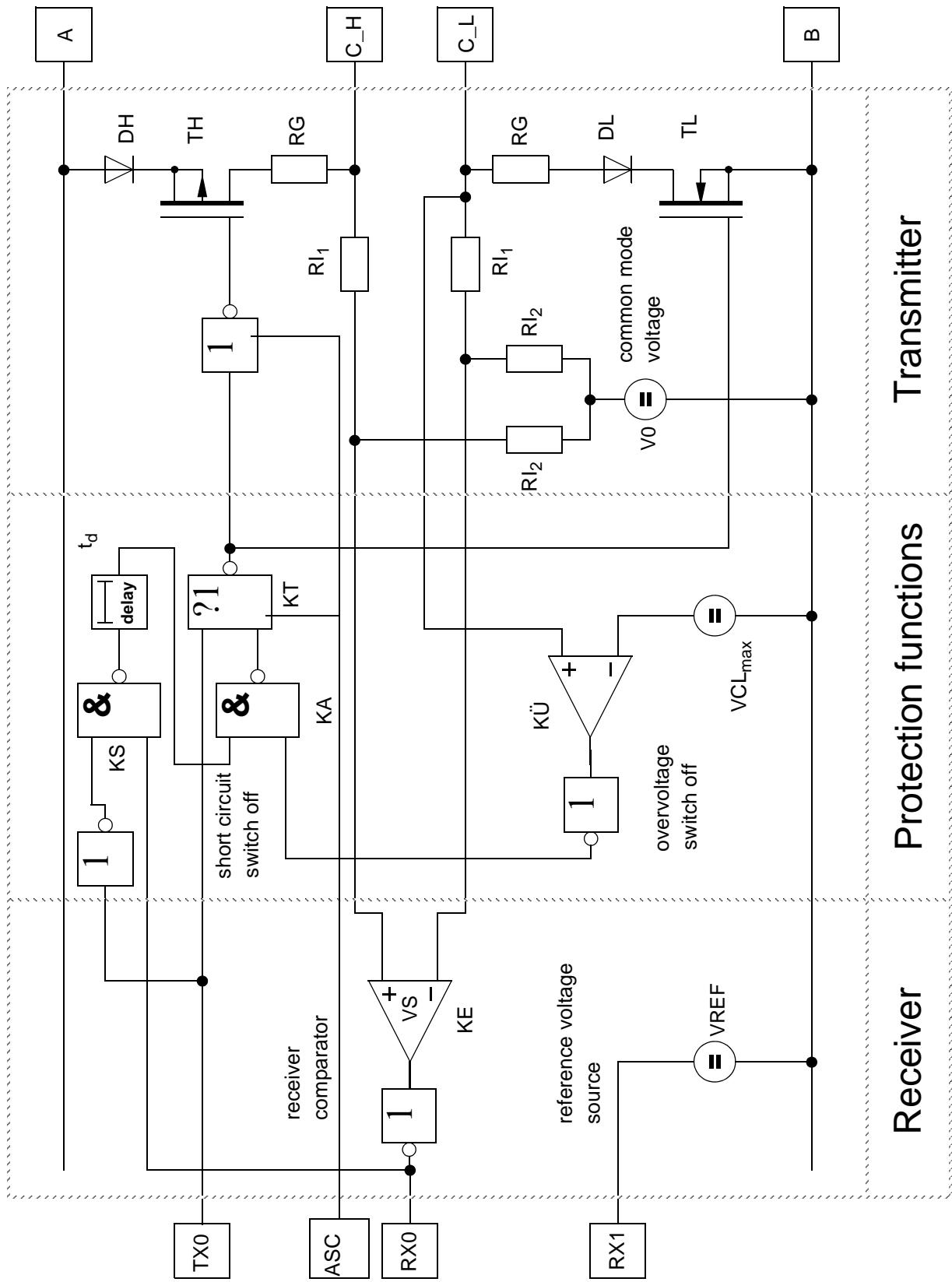
|   |                            |
|---|----------------------------|
| - difference input with constant switching threshold                  | $V_S$ (KE)                 |
| - input common mode voltage up to range above $V_A$ and below $V_B$   | KE                         |
| - reference voltage source for difference input of the CAN controller | $V_{REF}$                  |
| - data transmission rate up to 500 KBAUD                              | $C_H, C_L \rightarrow RX0$ |
| - symmetrical load of the bus   | KE                         |
| - Hysteresis of the receiver comparator                               |                            |

## 1.4 Properties of the monitoring functions

|   |        |
|---|--------|
| - locking of the transmission function in the event of short circuits at $C_L$ with $V_{C_L} > V_{C_Lmax}$  | KÜ     |
| - detection of short circuits between $C_H$ and $C_L$ as well as from $C_H$ to ground potential and switching off of the transmission function with a delay | KS, td |

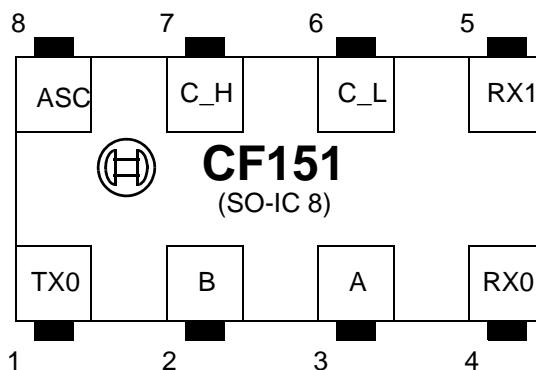


## 1.5 Block diagram CF151



**1.6 Connection pinout / pin designation / package**

|     |                                    | Pin |
|-----|------------------------------------|-----|
| TX0 | transmitter input                  | 1   |
| B   | ground                             | 2   |
| A   | supply voltage                     | 3   |
| RX0 | receiver output                    | 4   |
| RX1 | reference voltage                  | 5   |
| CL  | CAN bus terminal Low               | 6   |
| CH  | CAN bus terminal High              | 7   |
| ASC | rise time rate setting transmitter | 8   |



| characteristic value or connection point | conditions   | Symbol    | numeric value |       |                    | unit          |
|--|--|-----------|---------------|-------|--------------------|---------------|
|  |  |           | min.          | typ.  | max.               |               |
| <b>2. Data sheet</b>                     | All voltages are referred to pin B.<br><br>All numeric values under "typ" refer to $V_A=5V$ and $T_u=25\text{ C}$ .<br><br>General designations:<br><br>- RL: bus simulation 60 Ohm resistance between pin C_H and C_L.<br>- CL: capacitive load of 47pF between pin C_H and C_L.<br>- CRX0: capacitive load of maximum 20pF between pin RX0 and B.<br>- (rec): recessive transmitter ( $V_{TX0}=V_A$ )<br>- (dom): dominant transmitter ( $V_{TX0}=V_B$ ) |           |               |       |                    |               |
| <b>2.1 Maximum Ratings</b>               |  |           |               |       |                    |               |
| <b>2.1.1 Temperatures</b>                |  |           |               |       |                    |               |
| Ambient temperatures                     |  | $T_u$     | -40           | 125   | $^{\circ}\text{C}$ |               |
| Junction temperature                     |  | $T_j$     | -40           | 150   | $^{\circ}\text{C}$ |               |
| Storage temperature                      |  | $T_l$     | -40           | 150   | $^{\circ}\text{C}$ |               |
| <b>2.1.2 Maximum Ratings</b>             |  |           |               |       |                    |               |
| Pin A:<br>.1<br>Supply voltage           | (rec) for $V_A < 4.5\text{V}$ and $V_A > 5.5\text{V}$<br><br>no overvoltages and no undervoltages at C_H and C_L for $V_A > 5.5\text{V}$   | $V_A$     | -0.3          | 5     | 7                  | V             |
| Pin TX0:<br>.2<br>Input current          | $V_{TX0} > V_A$ due to dynamic switching peaks (no continuous operation)   | $I_{TX0}$ |               | 10    | mA                 |               |
|  | $V_{TX0} < V_B$ due to dynamic switching peaks (no continuous operation)   | $I_{TX0}$ | -10           |       | mA                 |               |
| .3<br>Input voltage                      | continuous operation $4.5\text{V} < V_A < 5.5\text{V}$   | $V_{TX0}$ | $V_B$         | $V_A$ |                    |               |
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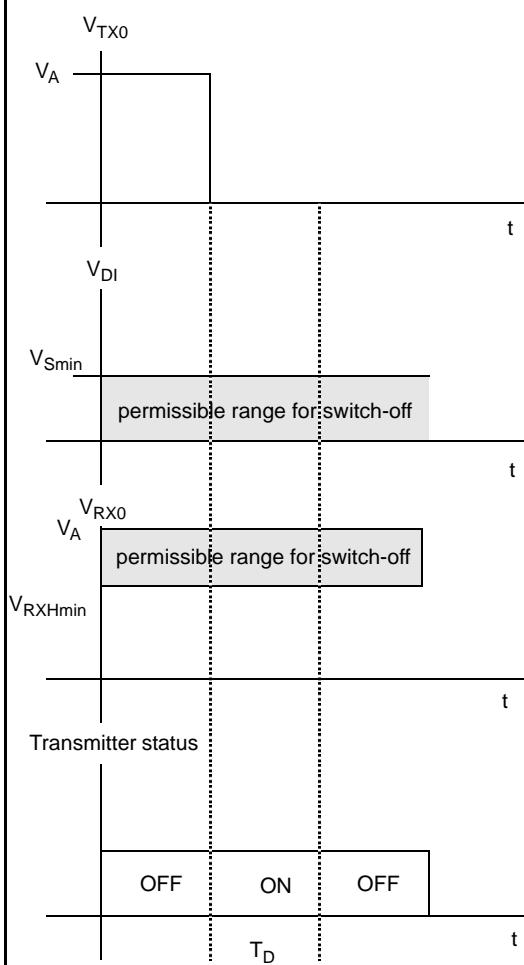
| characteristic value or connection point | conditions  | Symbol     | numeric value |      |       | unit          |
|--|---|------------|---------------|------|-------|---------------|
|  |   |            | min.          | typ. | max.  |               |
| Pin ASC:<br>.10<br>Input current         | $V_{ASC} > V_A$ due to dynamic switching peaks<br>(no continuous operation) | $I_{ASC}$  |               |      | 10    | mA            |
|  | $V_{ASC} < V_B$ due to dynamic switching peaks<br>(no continuous operation) | $I_{ASC}$  | -10           |      |       | mA            |
| .11<br>Input voltage                     | Continuous operation $4.5V < V_A < 5.5V$                                    | $V_{ASC}$  | $V_B$         |      | $V_A$ |               |
| Pin RX0:<br>.4<br>Output voltage         | No continuous operation   | $I_{RX0}$  | -1            |      | 1     | mA            |
| Pin RX1:<br>.5<br>Output voltage         |   | $V_{RX1}$  | $V_B$         |      | $V_A$ |               |
| Pin C_H:<br>.6.0<br>Input voltage        | (rec) for $V_A < 4.5V$  | $V_{C\_H}$ | -5            |      | 36    | V             |
| .6.1<br>Input voltage                    | $4.5 < V_A < 5.5V$  | $V_{C\_H}$ | -5            |      | 36    | V             |
| .7.0<br>Input current                    | (rec), $4.5V < V_A < 5.5V$ , $V_{C\_H} = 36V$                               | $I_{C\_H}$ |               | 5    |       | mA            |
| .7.1<br>Input current                    | (rec), $4.5V < V_A < 5.5V$ , $V_{C\_H} = -5V$                               | $I_{C\_H}$ | -3            |      |       | mA            |
| .7.2<br>Input current                    | (rec), $V_A = 0V$ , $V_{C\_H} = 36V$  | $I_{C\_H}$ |               | 5    |       | mA            |
| .7.3<br>Input current                    | (rec), $V_A = 0V$ , $V_{C\_H} = -5V$  | $I_{C\_H}$ | -3            |      |       | mA            |
| Pin C_L:<br>.8.0<br>Input voltage        | (rec) for $V_A < 4.5V$  | $V_{C\_L}$ | -5            |      | 36    | V             |
| .8.1<br>Input voltage                    | $4.5 < V_A < 5.5V$  | $V_{C\_L}$ | -5            |      | 36    | V             |
| .9.0<br>Input current                    | (rec), $4.5V < V_A < 5.5V$ , $V_{C\_L} = 36V$                               | $I_{C\_L}$ |               | 5    |       | mA            |
| .9.1<br>Input current                    | (rec), $4.5V < V_A < 5.5V$ , $V_{C\_L} = -5V$                               | $I_{C\_L}$ | -3            |      |       | mA            |
| .9.2<br>Input current                    | (rec), $V_A = 0V$ , $V_{C\_L} = 36V$  | $I_{C\_L}$ |               | 5    |       | mA            |
| .9.3<br>Input current                    | (rec), $V_A = 0V$ , $V_{C\_L} = -5V$  | $I_{C\_L}$ | -3            |      |       | mA            |
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|--|---|-------------------|----------------------------------|--------------|------|---------------|--|--|
|  |   |                   |                                  | Page - 8/17- |      |               |  |  |
| characteristic value or connection point   | conditions  | Symbol            | numeric value                    |              |      | unit          |  |  |
|  |   |                   | min.                             | typ.         | max. |               |  |  |
| <b>2.1.3 Electrical limits (disturbance pulses)</b><br><br>Pins C_H, C_L:<br>.1<br>Input voltage   | Disturbance pulses according to ISO/DIS 7637-1:1990(E), 4.6.3 Test Pulse 3a, 3b | V <sub>Puls</sub> | -150                             |              | 100  | V             |  |  |
| <b>2.1.4 Electrical limits (ESD)</b><br><br>all pins:<br>.1<br>ESD test voltage  | Human Body, 100pF, 1.5KOhm  | V <sub>ESD</sub>  | -2                               |              | 2    | KV            |  |  |
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| characteristic value or connection point | conditions   | Symbol    | numeric value |      |      | unit          |
|--|--|-----------|---------------|------|------|---------------|
|  |  |           | min.          | typ. | max. |               |
| <b>2.2 Characteristics</b>               |  |           |               |      |      |               |
| <b>2.2.1 Temperatures</b>                |  |           |               |      |      |               |
| Ambient temperatures                     |  | $T_u$     | -40           |      | 125  | °C            |
| Depletion layer temperatures             |  | $T_j$     | -40           |      | 150  | °C            |
| <b>2.2.2 Electrical Characteristics</b>  | unless specified otherwise it applies that:<br>$4.5V < V_A < 5.5V$<br>$V_{DI} = V_{C\_H} - V_{C\_L}$ |           |               |      |      |               |
| Pin A:<br>.1                             |  |           |               |      |      |               |
| Supply voltage                           |  | $V_A$     | 4.5           | 5    | 5.5  | V             |
| .2.0                                     |  |           |               |      |      |               |
| Current consumption                      | RL, (dom)  | $I_A$     |               |      | 80   | mA            |
| .2.1                                     |  |           |               |      |      |               |
| Current consumption                      | RL, (rec)  | $I_A$     |               |      | 20   | mA            |
| <b>Rise time setting</b>                 |  |           |               |      |      |               |
| Pin ASC:<br>.28                          |  |           |               |      |      |               |
| Input capacitance                        | $V_B < V_{ASC} < V_A$  | $C_{IN}$  |               | 25   |      | pF            |
| .29                                      |  |           |               |      |      |               |
| Input current                            | $V_{ASC} = V_A$ , (internal pulldown)  | $I_{ASC}$ |               |      | 275  | µA            |
| .30                                      |  |           |               |      |      |               |
| Input current                            | $V_{ASC} = V_B$  | $I_{ASC}$ | -2            | 0    | 2    | µA            |
| .31                                      |  |           |               |      |      |               |
| Input level<br>Low Speed                 | RL, CL   | $V_{ASH}$ | 0.8           |      | 1    | * $V_A$       |
| .32                                      |  |           |               |      |      |               |
| Input level<br>High Speed                | RL, CL   | $V_{ASL}$ | 0             |      | 0.2  | * $V_A$       |
| <b>Transmitter input DC</b>              |  |           |               |      |      |               |
| Pin TX0:<br>.3                           |  |           |               |      |      |               |
| Input capacitance                        | $V_B < V_{TX0} < V_A$  | $C_{IN}$  |               | 25   |      | pF            |
| .4                                       |  |           |               |      |      |               |
| Input current                            | (dom), (internal pullup)   | $I_{TX0}$ | -275          |      |      | µA            |
| .5                                       |  |           |               |      |      |               |
| Input current                            | (rec)  | $I_{TX0}$ | -2            | 0    | 2    | µA            |
| .6                                       |  |           |               |      |      |               |
| Input level<br>High level                | RL, $V_{DI} < V_{DIHmax}$  | $V_{TXH}$ | 0.7           |      | 1    | * $V_A$       |
| .7                                       |  |           |               |      |      |               |
| Input level<br>Low level                 | RL, $V_{DI} > V_{DIHmin}$  | $V_{TXL}$ | 0             |      | 0.3  | * $V_A$       |
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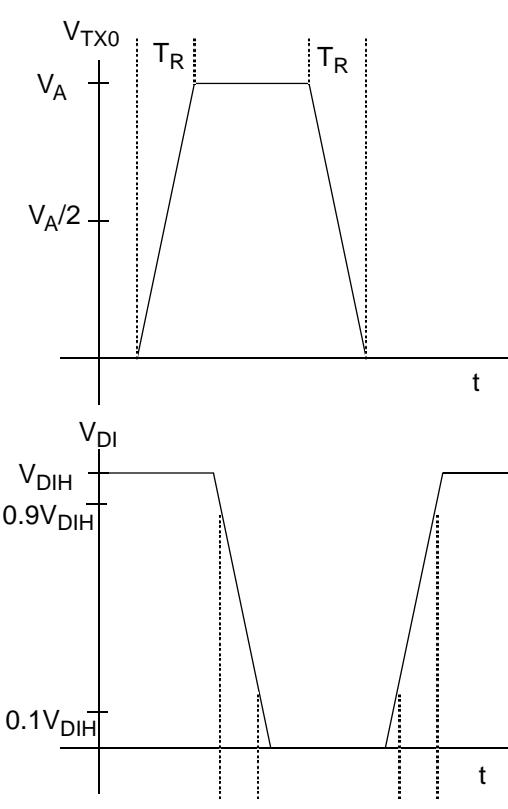
| characteristic value or connection point                   | conditions  | Symbol           | numeric value |      |      | unit            |
|--|---|------------------|---------------|------|------|-----------------|
|  |   |                  | min.          | typ. | max. |                 |
| <b>Transmitter outputs DC</b>                              |   |                  |               |      |      |                 |
| Pins C_H, C_L:<br>.8<br>Output quiescent voltage C_H<br>.9 | (rec)   | V <sub>CHR</sub> | 0.4           | 0.5  | 0.6  | *V <sub>A</sub> |
| Output quiescent voltage C_L<br>.10                        | (rec)   | V <sub>CLR</sub> | 0.4           | 0.5  | 0.6  | *V <sub>A</sub> |
| Output quiescent current C_H<br>.11                        | (rec), -2V < V <sub>CH</sub> < 7V   | I <sub>CHR</sub> | -0.7          |      | 0.7  | mA              |
| Output quiescent current C_H<br>.12                        | (rec), 1V < V <sub>CH</sub> < 4V  | I <sub>CHR</sub> | -0.3          |      | 0.3  | mA              |
| Output quiescent current C_L<br>.13                        | (rec), -2V < V <sub>CL</sub> < 7V   | I <sub>CLR</sub> | -0.7          |      | 0.7  | mA              |
| Output quiescent current C_L<br>.14                        | (rec), 1V < V <sub>CH</sub> < 4V  | I <sub>CLR</sub> | -0.3          |      | 0.3  | mA              |
| Output difference voltage<br>.14.1<br>recessive            | V <sub>DIL(H)</sub> = V <sub>C_H</sub> - V <sub>C_L</sub>   | V <sub>DIL</sub> | -500          | 0    | 50   | mV              |
| .14.2<br>dominant  | V <sub>TX0</sub> > V <sub>TXHmin</sub><br>RL, V <sub>TX0</sub> < V <sub>TXLmax</sub>  | V <sub>DIH</sub> | 1.5           |      | 3    | V               |
| <b>Receiver input DC</b>                                   |   |                  |               |      |      |                 |
| Pins C_H, C_L<br>.16<br>threshold voltage<br>.33           | V <sub>S</sub> = V <sub>C_H</sub> - V <sub>C_L</sub>  | V <sub>S</sub>   | 500           | 700  | 900  | mV              |
| hysteresis voltage<br>.17                                  |   | V <sub>HYS</sub> |               | 200  |      | mV              |
| Input common mode voltage                                  | V <sub>COM</sub> = (V <sub>C_H</sub> + V <sub>C_L</sub> ) / 2 with<br>V <sub>C_Hmax</sub> = V <sub>C_Lmax</sub> = 7V<br>V <sub>C_Hmin</sub> = V <sub>C_Lmin</sub> = -2V | V <sub>COM</sub> | -2            |      | 7    | V               |
| <b>Receiver output DC</b>                                  |   |                  |               |      |      |                 |
| Pin RX0:   |   |                  |               |      |      |                 |
| Output voltage<br>.18                                      |   |                  |               |      |      |                 |
| Low level<br>.19   | V <sub>DI</sub> > 0.9V<br>-0.3mA < I <sub>RX0</sub> < 1mA<br>V <sub>B</sub> < V <sub>RX0</sub> < V <sub>A</sub>   | V <sub>RXL</sub> |               |      | 0.5  | V               |
| High level   | V <sub>DI</sub> < 0.5V<br>-0.3mA < I <sub>RX0</sub> < 0.3mA<br>V <sub>B</sub> < V <sub>RX0</sub> < V <sub>A</sub>   | V <sub>RXH</sub> | 0.9           |      |      | *V <sub>A</sub> |
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|--|--|-----------------|----------------------------------|---------------|------|-----------------|----|------|
|  |  |                 |                                  | Page - 11/17- |      |                 |    |      |
| characteristic value or connection point   | conditions   | Symbol          | numeric value                    |               |      | unit            |    |      |
|  |  |                 | min.                             | typ.          | max. |                 |    |      |
| <b>Reference voltage source</b>  |  |                 |                                  |               |      |                 |    |      |
| Pin RX1:<br>.20<br>Output voltage<br>.21<br>Output resistance  | $I_{RX1}=0A$<br>$RA=dV_{RX1}/dI_{RX1}$ at the point<br>$V_{RX1}=V_A/2$ | $V_{RX1}$<br>RA | 0.45                             | 0.5           | 0.55 | *V <sub>A</sub> | 20 | kOhm |
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| characteristic value or connection point                                 | conditions  | Symbol           | numeric value |      |      | unit          |
|--|---|------------------|---------------|------|------|---------------|
|  |   |                  | min.          | typ. | max. |               |
| <b>Protection circuit</b>  |   |                  |               |      |      |               |
| Pin C_L:<br>.22<br>Response threshold of the overvoltage switch-off      | In the event of short-circuits between bus cables and/or overvoltages and/or ground, the CAN controller components switches the transmitter stage off after 20 to 80 bit times, but after 1 ms at the maximum.  | V <sub>CLM</sub> | 7             | 8    | 10   | V             |
| Pin C_H, C_L:<br>.23<br>Switch-off delay time at short-circuit detection | Short-circuit resistance RKS<1Ohm<br><br>Switch-off condition and definition of T <sub>D</sub><br>ON: TH, TL are switched on<br>OFF: TH, TL are switched off  | T <sub>D</sub>   | 1.5           | 5.5  | 10   | μs            |
|  |  <p>The diagram illustrates the logic levels and timing for the protection circuit. It shows five voltage waveforms over time <math>t</math>:</p> <ul style="list-style-type: none"> <li><math>V_A</math>: Logic level.</li> <li><math>V_DI</math>: Diode protection level.</li> <li><math>V_{Smin}</math>: Minimum signal level.</li> <li><math>V_{TX0}</math>: Transmitter output level.</li> <li><math>V_{RX0}</math>: Receiver input level.</li> </ul> <p>Two shaded regions indicate the "permissible range for switch-off" for the transmitter (<math>V_{TX0}</math>) and receiver (<math>V_{RX0}</math>). The transmitter status is shown as OFF, ON, OFF. The switch-off delay <math>T_D</math> is indicated by a horizontal bar at the bottom.</p> |                  |               |      |      |               |
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| characteristic value or connection point                                | conditions   | Symbol   | numeric value |      |      | unit          |
|---|--|----------|---------------|------|------|---------------|
|   |  |          | min.          | typ. | max. |               |
| Pin A:<br>.24<br>Peak current consumption<br>in short-circuit operation | (dom), short-circuit Pin C_H to B<br>(limited to $T_{max}=10\mu s$ by short-circuit detection) | $I_{AM}$ |               | 200  |      | mA            |
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| characteristic value or connection point  | conditions   | Symbol                 | numeric value |      |      | unit |
|---|--|------------------------|---------------|------|------|------|
|   |  |                        | min.          | typ. | max. |      |
| <b>Transmitter AC</b><br>.25<br>Signal delay time from<br>Transmitter input TX0 to<br>Transmitter output C_H, C_L | RL, CL, $T_R < 2\text{ns}$ , $V_{ASC} < V_{ASL}$<br>$V_A = 5\text{V}$ , $T_u = 25^\circ\text{C}$ | $T_{VSF}$<br>$T_{VSR}$ |               |      | 50   | ns   |
|   |  |                        |               |      | 50   | ns   |

| characteristic value or connection point                       | conditions   | Symbol   | numeric value |      |      | unit             |
|--|--|----------|---------------|------|------|------------------|
|  |  |          | min.          | typ. | max. |                  |
| <b>Transmitter AC</b>  |  |          |               |      |      |                  |
| .25a<br>Rise time rate transmitter output, High Speed C_H, C_L | RL, CL, $T_R < 2\text{ns}$ , $V_{ASC} < V_{ASL}$<br>$V_A = 5V$ , $T_u = 25^\circ\text{C}$  | $S_{AH}$ | 20            |      | 50   | V/ $\mu\text{s}$ |
| .25b<br>Fall time rate transmitter output, High Speed C_H, C_L | RL, CL, $T_R < 2\text{ns}$ , $V_{ASC} < V_{ASL}$<br>$V_A = 5V$ , $T_u = 25^\circ\text{C}$  | $S_{FH}$ | 20            |      | 50   | V/ $\mu\text{s}$ |
| .25c<br>Rise time rate transmitter output, Low Speed C_H, C_L  | RL, CL, $T_R < 2\text{ns}$ , $V_{ASC} > V_{ASH}$<br>$V_A = 5V$ , $T_u = 25^\circ\text{C}$  | $S_{AL}$ | 5             |      | 20   | V/ $\mu\text{s}$ |
| .25d<br>Fall time rate transmitter output, Low Speed C_H, C_L  | RL, CL, $T_R < 2\text{ns}$ , $V_{ASC} > V_{ASH}$<br>$V_A = 5V$ , $T_u = 25^\circ\text{C}$  | $S_{FL}$ | 5             |      | 20   | V/ $\mu\text{s}$ |
|  |  $S_{XX} = (0.9V_{DIH} - 0.1V_{DIH}) / T_{XX}$ |          |               |      |      |                  |
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| characteristic value or connection point                            | conditions   | Symbol                 | numeric value |      |      | unit          |
|---|--|------------------------|---------------|------|------|---------------|
|   |  |                        | min.          | typ. | max. |               |
| <b>Transmitter and receiver AC</b>                                  |  |                        |               |      |      |               |
| .26   |  |                        |               |      |      |               |
| Signal delay time from Transmitter input TX0 to Receiver output RX0 | RL, CL, CRX0, $T_R < 2\text{ns}$ , CUH, CUL<br>$V_{ASC} < V_{ASL}$<br>$V_A = 5\text{V}$ , $T_u = 25^\circ\text{C}$ | $T_{VGR}$<br>$T_{VGF}$ | 280           | 280  | ns   | ns            |
|   |  |                        |               |      |      |               |
| Date: 09/01/10  |  |                        |               |      |      | Dept.:AE/EIC1 |



| characteristic value or connection point   | conditions   | Symbol                 | numeric value |      |      | unit     |
|--|--|------------------------|---------------|------|------|----------|
|  |  |                        | min.          | typ. | max. |          |
| <b>Receiver AC</b><br>.27<br>Signal delay time from<br>Receiver input C_H, C_L to<br>after Receiver output RX0 | CRX0, $T_R < 2\text{ns}$<br>$V_A = 5\text{V}$ , $T_u = 25^\circ\text{C}$ | $T_{VEF}$<br>$T_{VER}$ |               | 150  | 150  | ns<br>ns |

